

# P-Channel MOSFET

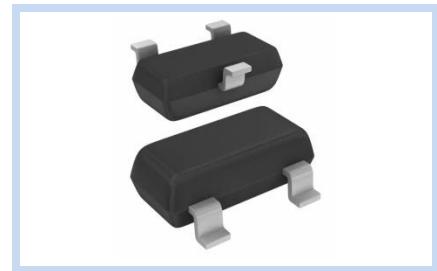
## 80V 1.9A SOT-23 ESD

MFT8P1A9S23E

MERITEK

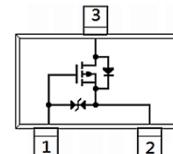
### FEATURE

- $R_{DS(ON)} \leq 135\text{m}\Omega$ ,  $V_{GS} = -10\text{V}$
- Super High Dense Cell Design for Extremely Low  $R_{DS(ON)}$
- ESD Protected Gate and Fast Switching Characteristic
- Green Device Available



### MECHANICAL DATA

- Case: SOT-23 Package
- Terminals: Solderable per MIL-STD-750, Method 2026



### MAXIMUM RATINGS

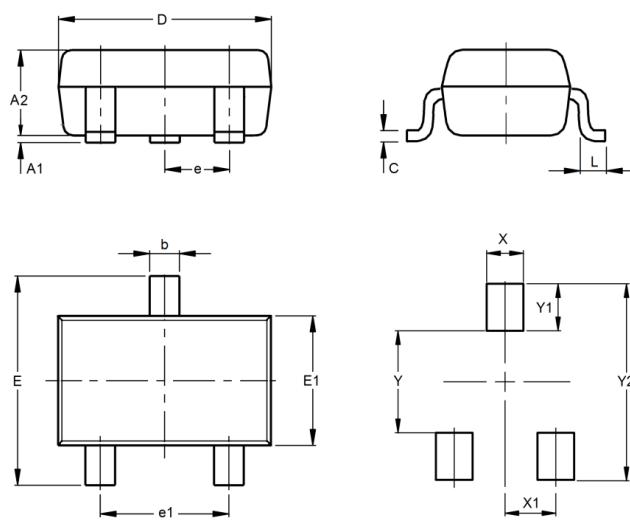
Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	-80	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	-1.9	A
		-1.5	
Pulsed Drain Current	$I_{DM}$	-7.5	A
Continuous Body Diode Forward Current	$I_s$	-1.9	
Power Dissipation	$P_D$	0.95	W
		0.60	
Thermal Maximum, Junction to Ambient	$R_{eJA}$	132	$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^{\circ}\text{C}$

Note:

1. The value of  $R_{eJA}$  is measured with the device mounted on 1 in<sup>2</sup> FR FR-4 board with 2 oz. copper, in a still air environment with  $T(T_A=25^{\circ}\text{C})$ .
2. The power dissipation  $P(P_D)$  is based on  $R_{eJA}$  and the maximum allowed junction temperature of 150°C.
3. The value in any given application depends on the user's specific board design. Surface Mounted on FR4 Board,  $t < 10$  sec.
4. Repetitive rating, pulse width limited by junction temperature  $T_{J(MAX)}= 150^{\circ}\text{C}$ . Ratings are based on low frequency to keep initial  $T(T_J=25^{\circ})$ .

### DIMENSIONS

Item	Min (mm)	Max (mm)
A1	--	0.10
A2	0.79	1.30
b	0.30	0.50
C	0.08	0.20
D	2.70	3.10
e	0.89	1.02
e1	1.78	2.04
E	2.10	2.80
E1	1.20	1.60
L	0.15	--
X	0.80	
X1	0.95	
Y	1.40	
Y1	1.00	
Y2	3.40	



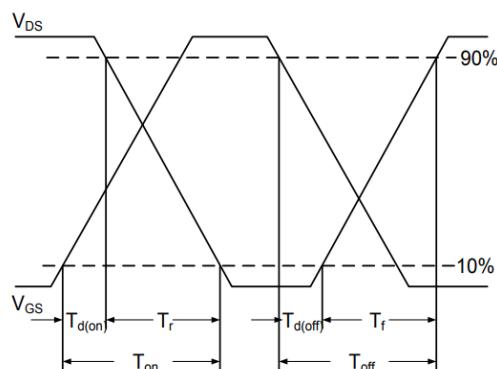
## ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Drain-Source Breakdown Voltage</b>	$V_{GS} = 0V, I_D = -250\mu A$	$BV_{DSS}$	-80	-	-	V
<b>Forward Transconductance</b>	$V_{DS} = -10V, I_D = -3A$	$G_{FS}$	-	6	-	S
<b>Gate-Body Leakage Current</b>	$V_{GS} = \pm 16V, V_{DS} = 0V$	$I_{GSS}$	-	-	$\pm 10$	$\mu A$
<b>Zero Gate Voltage Drain Current</b>	$V_{GS} = 0V, V_{DS} = -64V$	$I_{DSS}$	-	-	-1	
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Gate Threshold Voltage</b>	$V_{GS} = V_{DS}, I_D = -250\mu A$	$V_{GS(th)}$	-1	-	-2.5	V
<b>Static Drain Source On-Resistance</b>	$V_{GS} = -10V, I_D = -1A$	$R_{DS(ON)}$	-	104	135	$m\Omega$
	$V_{GS} = -4.5V, I_D = -0.5A$		-	140	185	
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Input Capacitance</b>	$V_{GS} = 0V, V_{DS} = -40V, f = 1MHz$	$C_{iss}$	-	520	-	pF
<b>Output Capacitance</b>		$C_{oss}$	-	48	-	
<b>Reverse Transfer Capacitance</b>		$C_{rss}$	-	36	-	
<b>Gate Resistance</b>	$f = 1MHz$	$R_g$	-	7	-	$\Omega$
<b>Total Gate Charge</b>	$V_{GS} = -5V, V_{DS} = -40V, I_D = -1A$	$Q_g$	-	7.4	-	nC
<b>Gate-Source Charge</b>		$Q_{gs}$	-	2.2	-	
<b>Gate-Drain Charge</b>		$Q_{gd}$	-	3.2	-	
<b>Turn-On Delay Time</b>	$V_{GS} = -10V, V_{DS} = -40V, I_D = -1A, R_{GS} = 10\Omega$	$t_{d(on)}$	-	7.4	-	ns
<b>Turn-On Rise Time</b>		$t_r$	-	17	-	
<b>Turn-Off Delay Time</b>		$t_{d(off)}$	-	36	-	
<b>Turn-Off Fall Time</b>		$t_f$	-	25	-	
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Diode Forward Voltage</b>	$V_{GS} = 0V, I_S = -1A$	$V_{SD}$	-	-0.8	-1.2	V
<b>Reverse Recovery Time</b>	$I_F = -1A, dI_F/dt = 100A/us$	$trr$	-	14	-	ns
<b>Reverse Recovery Charge</b>		$Qrr$	-	9.5	-	nC

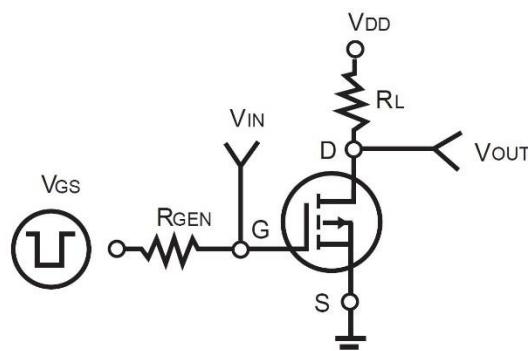
Note:

1. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
2. Independent of operating temperature.

Switching Time Waveform

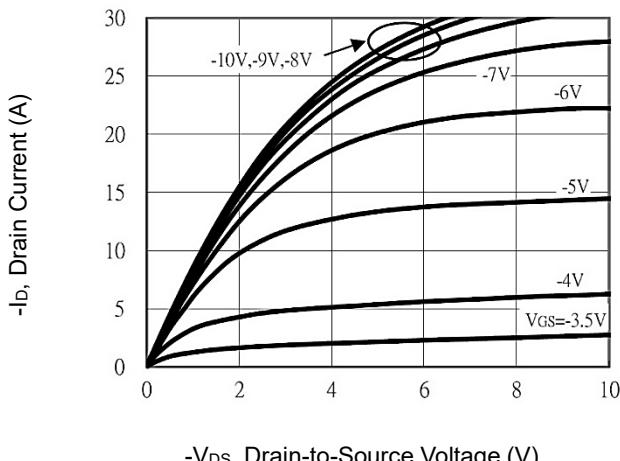


Switching Test Circuit

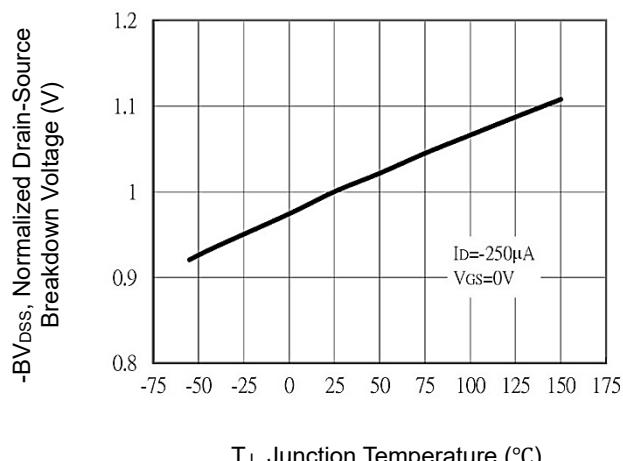


## CHARACTERISTIC CURVES

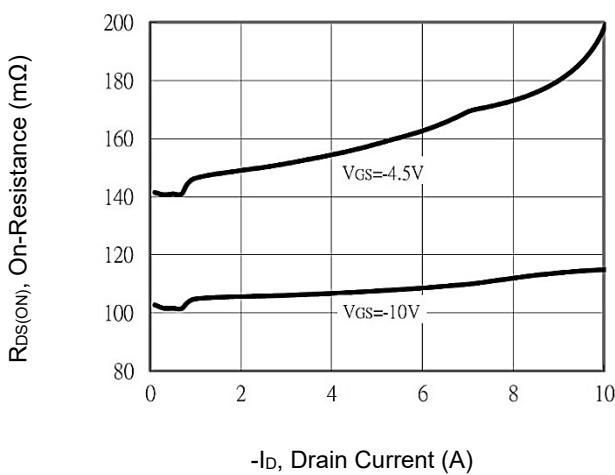
Typical Output Characteristics



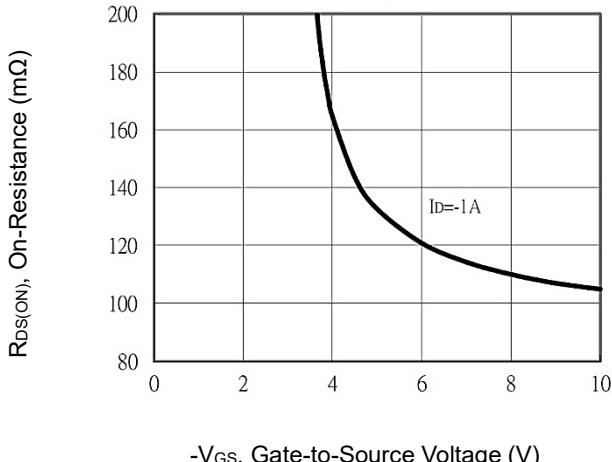
Breakdown Voltage vs. Ambient Temperature



Static Drain-Source  
On-State Resistance v.s. Drain Current

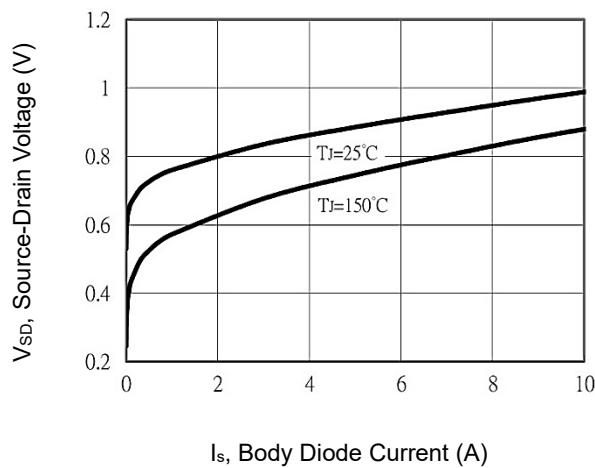


Static Drain-Source On-State  
Resistance vs Gate-Source Voltage

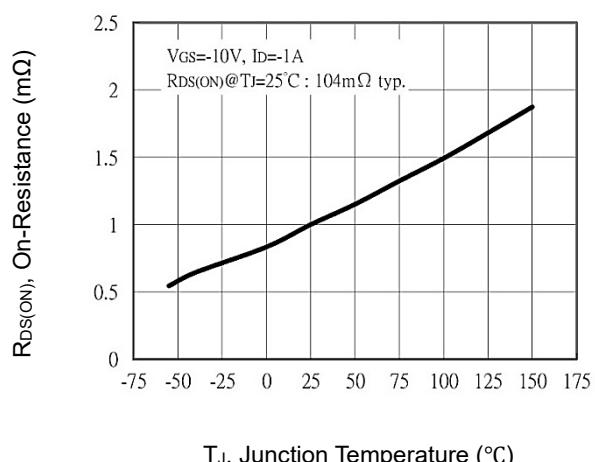


$T_J$ , Junction Temperature (°C)

Body Diode Current vs.  
Source-Drain Voltage



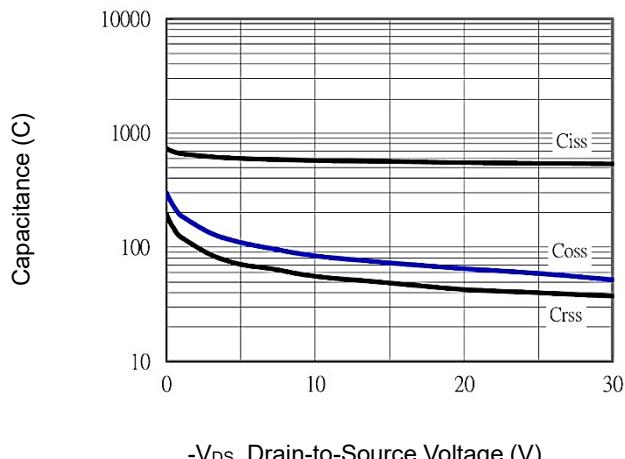
$I_S$ , Body Diode Current (A)  
Drain-Source On-State Resistance  
v.s. Junction Temperature



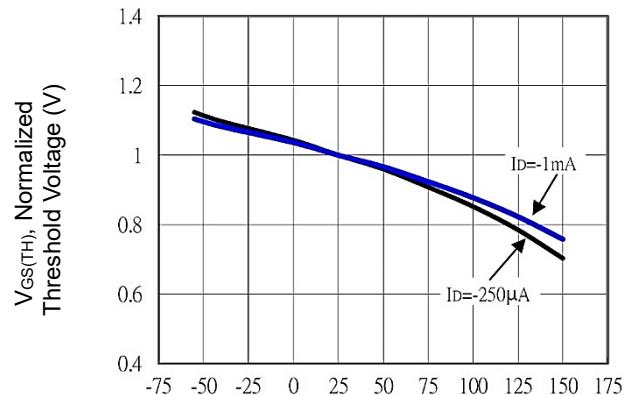
$T_J$ , Junction Temperature (°C)

## CHARACTERISTIC CURVES

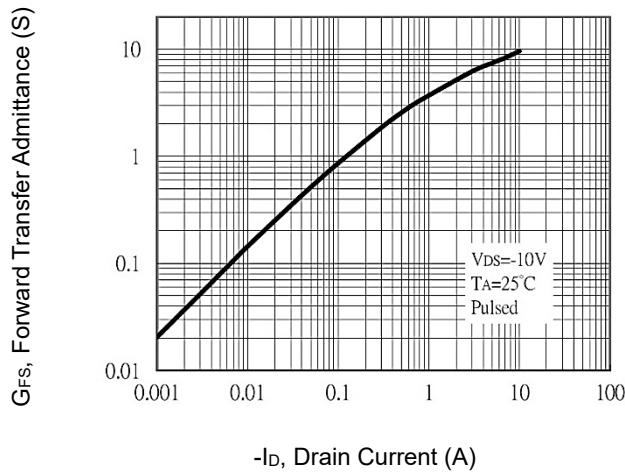
Capacitance vs Drain Drain-to-Source Voltage



Threshold Voltage vs. Junction Temperature

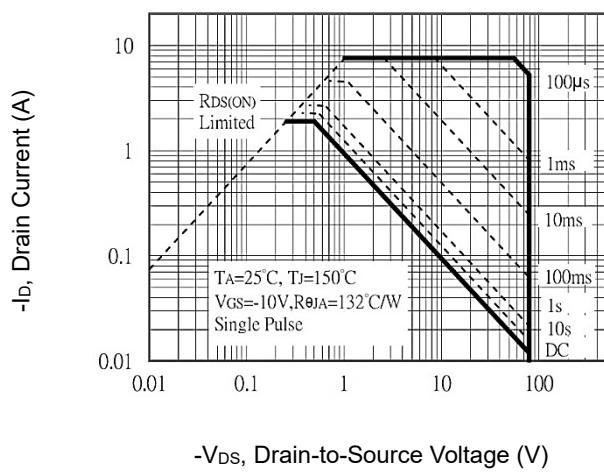


Forward Transfer Admittance vs. Drain Current

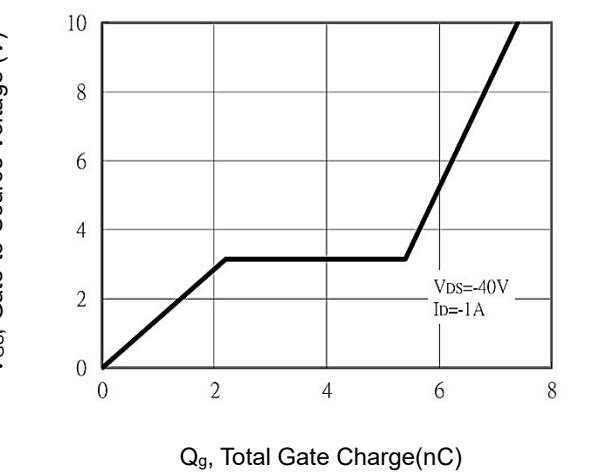


$-I_D$ , Drain Current (A)

### Safe Operation Area

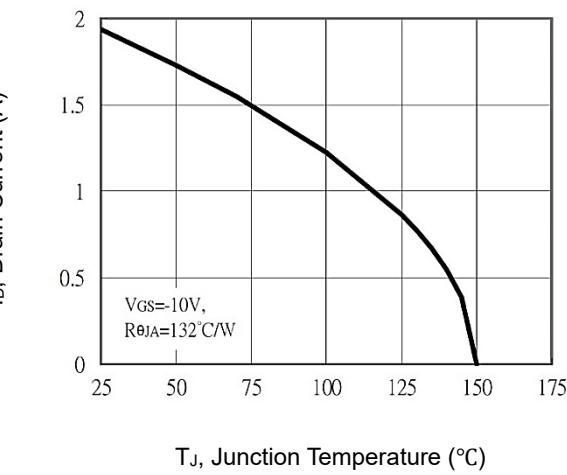


$-V_{DS}$ , Drain-to-Source Voltage (V)



$Q_g$ , Total Gate Charge(nC)

### Maximum Drain Current vs. Junction Temperature



$T_J$ , Junction Temperature (°C)

## CHARACTERISTIC CURVES

Single Pulse Power Rating, Junction to Ambient

