

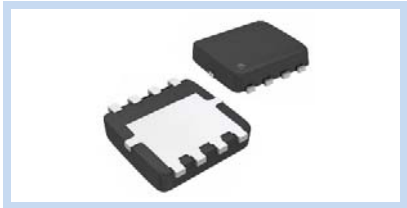
P-Channel MOSFET
30V 50A 60W DFN3333-8L

MFT3P50P33

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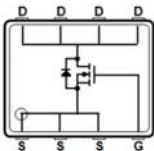
FEATURE

- $R_{DS(ON)} < 8.5m\Omega$, $V_{GS} = -10V$, $I_D = -10.0A$
- $R_{DS(ON)} < 14m\Omega$, $V_{GS} = -4.5V$, $I_D = -8.0A$
- Improved dv/dt capability



MECHANICAL DATA

- Case: DFN3333-8L package
- Terminal: Solderable per MIL-STD-750, Method 2026

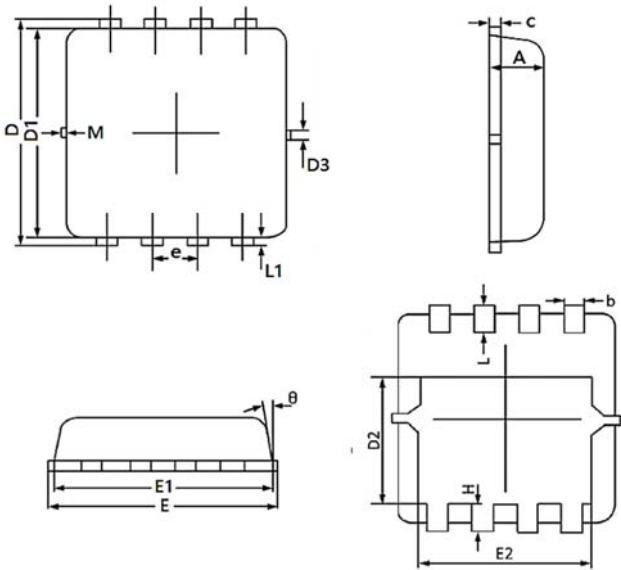


MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current – Continuous	$T_C = 25^\circ C$	I_D	-50	A
	$T_C = 100^\circ C$		-32	
	$T_A = 25^\circ C$		-10	
	$T_A = 70^\circ C$		-8	
Drain Current – Pulsed	$T_C = 25^\circ C$	I_{DM}	-200	A
Power Dissipation	$T_C = 25^\circ C$	P_D	60	W
	$T_C = 100^\circ C$		24	
	$T_A = 25^\circ C$		2.0	
	$T_A = 70^\circ C$		1.3	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ C$
Typical Thermal Resistance, Junction to Case		$R_{\theta JC}$	2.1	$^\circ C/W$
Typical Thermal Resistance, Junction to Ambient		$R_{\theta JA}$	62.5	

DIMENSIONS

Item	Min	Max
A	0.70	0.80
b	0.25	0.35
c	0.10	0.25
D	3.25	3.45
D1	3.00	3.20
D2	1.78	1.98
D3	0.130 REF	
E	3.20	3.40
E1	3.00	3.20
E2	2.39	2.59
e	0.650 BSC	
H	0.30	0.50
L	0.30	0.50
L1	0.130 REF	
θ	0°	12°



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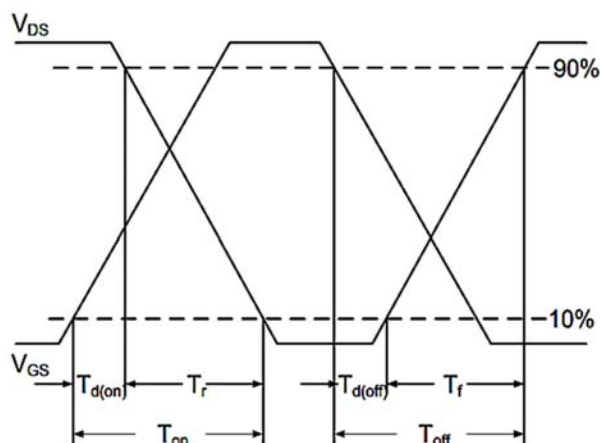
ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	BV_{DSS}	-30	--	--	V
Zero Gate Voltage Drain Current	$V_{DS}=-30V, V_{GS}=0V$	I_{DSS}	--	--	-1.0	μA
Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	--	--	± 100	nA
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-10A$	$R_{DS(ON)}$	--	7.1	8.5	m Ω
	$V_{GS}=-4.5V, I_D=-8A$		--	10	14	
Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	$V_{GS(th)}$	-1.0	-1.5	-2.5	V
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Total Gate Charge	$V_{DS}=-15V, I_D=-10A$ $V_{GS}=-4.5V$	Q_g	--	27	--	nC
Gate-Source Charge		Q_{gs}	--	8.4	--	
Gate-Drain Charge		Q_{gd}	--	8.7	--	
Turn-On Delay Time	$V_{DD}=-15V, I_D=-1A$ $V_{GS}=-10V, R_G=6\Omega$	$T_{d(on)}$	--	10	--	nS
Rise Time		T_r	--	13	--	
Turn-Off Delay Time		$T_{d(off)}$	--	111	--	
Fall Time		T_f	--	51	--	
Input Capacitance	$V_{DS}=-15V, V_{GS}=0V$ $F=1MHz$	C_{iss}	--	3228	--	pF
Output Capacitance		C_{oss}	--	396	--	
Reverse Transfer Capacitance		C_{rss}	--	254	--	
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
Continuous Source Current	--	I_S	--	--	-50	A
Diode Forward Voltage	$V_{GS}=0V, I_S=-1A$	V_{SD}	--	-0.7	-1	V

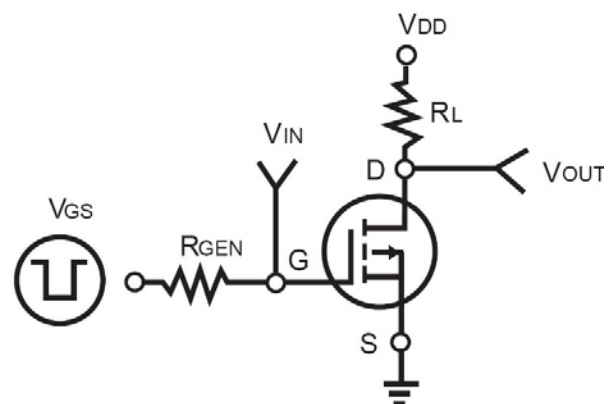
Note:

1. Pulse width < 300 μs , Duty cycle < 2%
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^{\circ}C$. Ratings are based on low frequency and duty cycles to keep initial $T_J=25^{\circ}C$.
4. The maximum current rating is package limited
5. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.
Mounted on a 1 inch² with 2oz. square pad of copper
6. Guaranteed by design, not subject to production testing.

Switching Time Waveform



Switching Test Circuit



P-Channel MOSFET

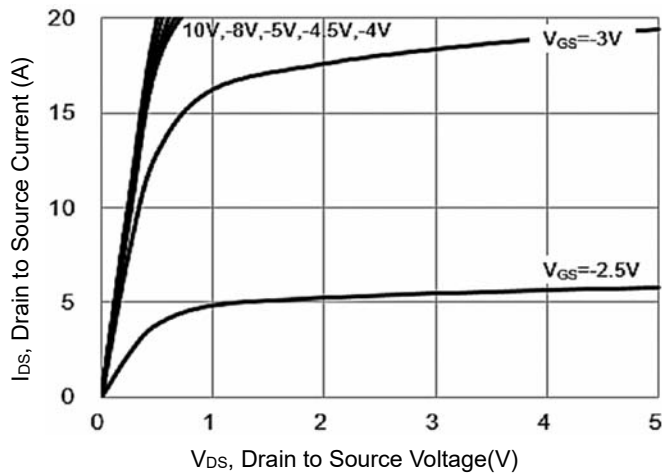
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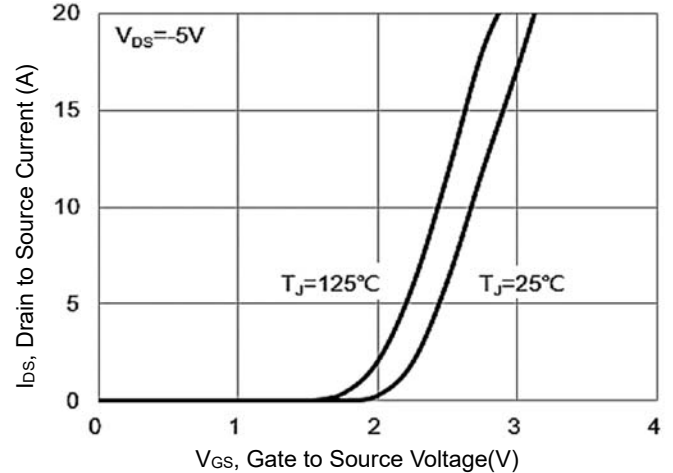
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CHARACTERISTIC CURVES

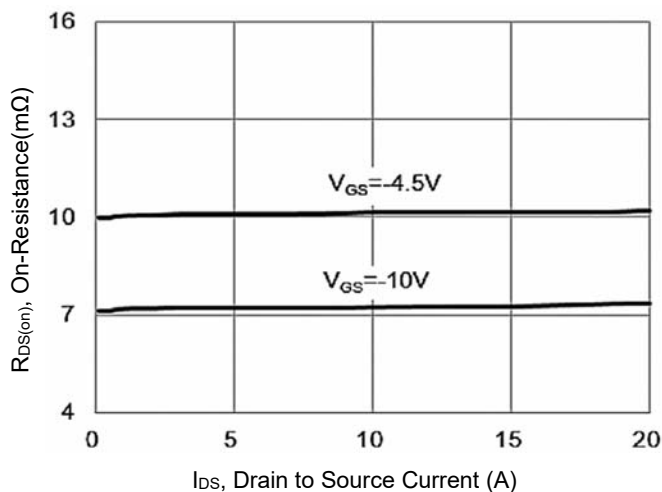
On-Region Characteristic



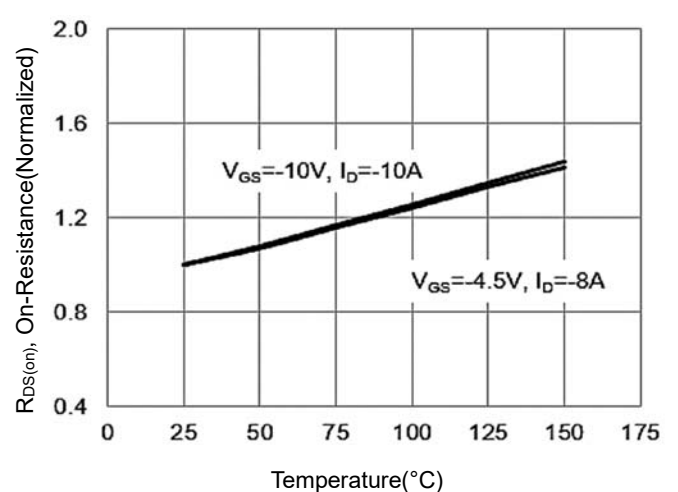
Transfer Characteristics



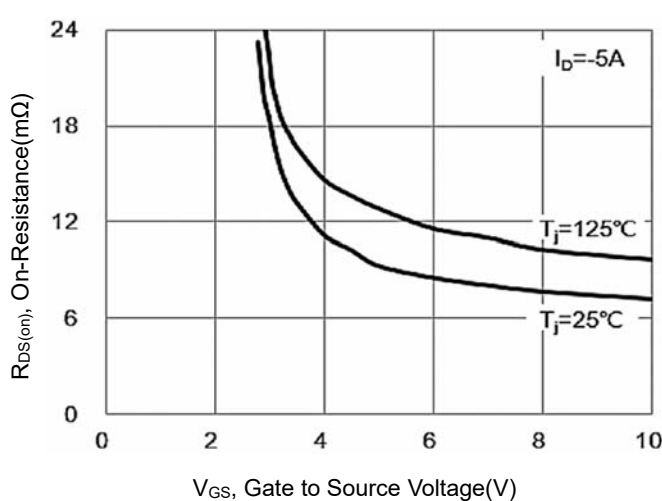
On-Resistance vs. Drain Current



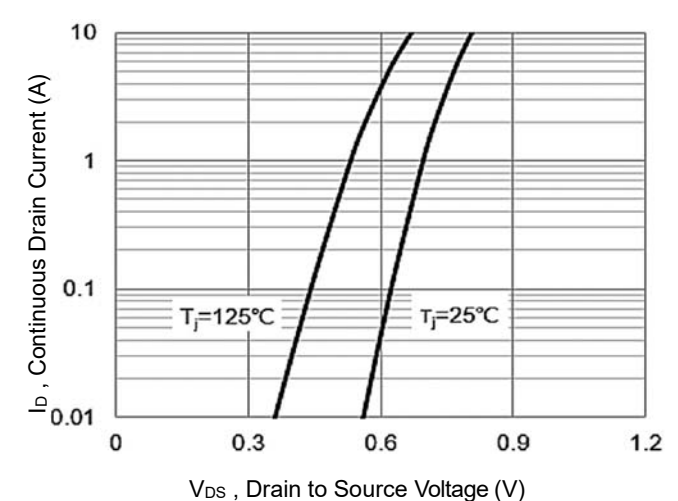
On-Resistance vs. Junction Temperature



On-Resistance Variation with VGS



Source-Drain Diode Forward Voltage



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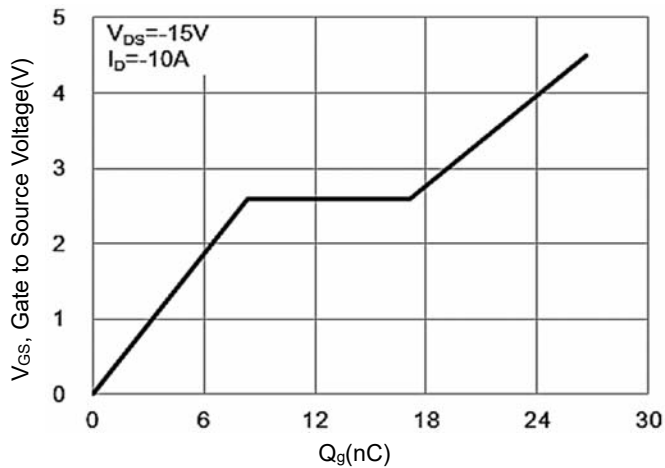
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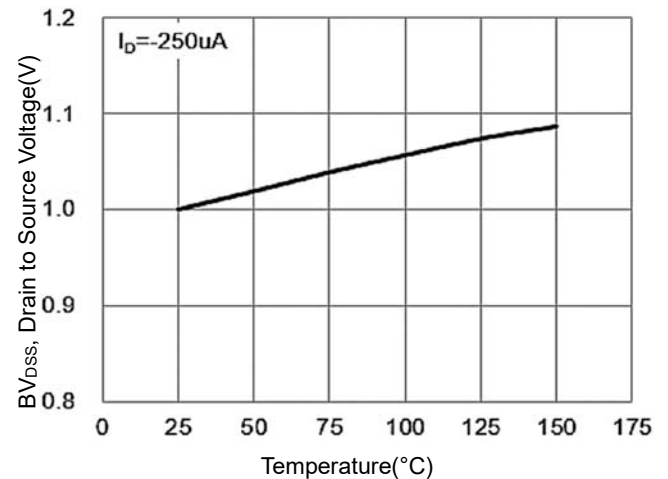
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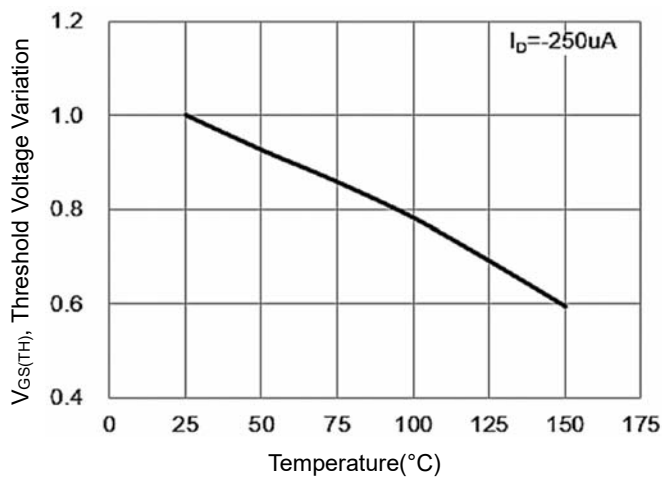
Gate-Charge Characteristics



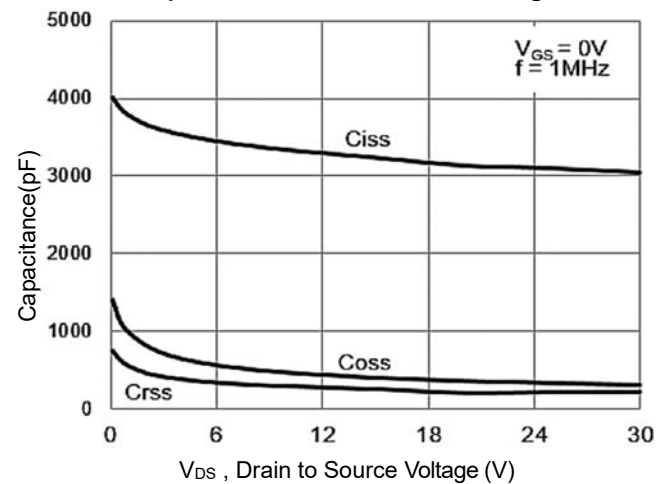
Breakdown Voltage Variation vs. Temperature



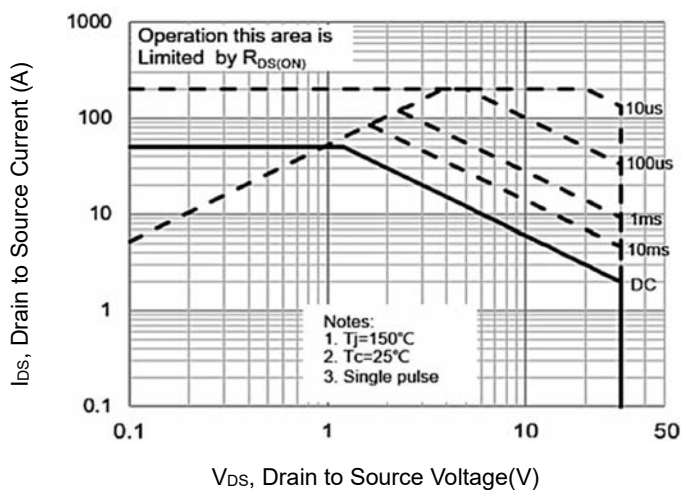
Threshold Voltage Variation with Temperature



Capacitance vs. Drain-Source Voltage



Maximum Safe Operating Area



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CHARACTERISTIC CURVES

