

P-Channel MOSFET

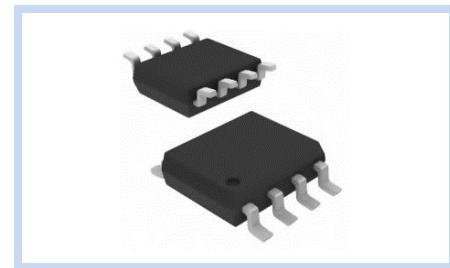
30V 25A 12W SOP-8

MFT3P25S8

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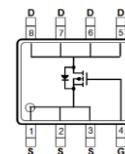
FEATURE

- $R_{DS(ON)} < 10\text{m}\Omega$, $V_{GS} = -10\text{V}$, $I_D = -10\text{A}$
- $R_{DS(ON)} < 16\text{m}\Omega$, $V_{GS} = -4.5\text{V}$, $I_D = -8\text{A}$
- Advanced Trench Process Technology
- Low Gate Charge
- Fast Switching Characteristic



MECHANICAL DATA

- Case: SOP-8 Package
- Terminals: Solderable per MIL-STD-750, Method 2026



MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current – Continuous	I_D	-25	A
		-16	
		-11	
		-9	
Drain Current – Pulsed	I_{DM}	-100	A
Continuous Body Diode Forward Current	I_S	-10	A
Avalanche Current	I_{AS}	-30	A
Avalanche Energy	E_{AS}	64	mJ
Power Dissipation	P_D	12	W
		4.8	
		2.3	
		1.5	
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	55	°C/W
Thermal Resistance, Junction to Case	$R_{\theta JC}$	10.5	
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

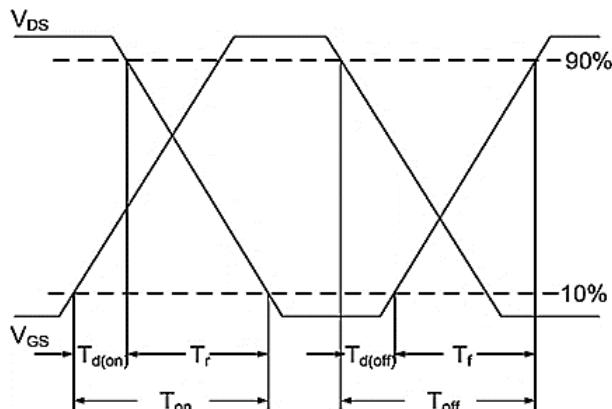
ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	BV_{DSS}	-30	--	--	V
Drain-Source Leakage Current	$V_{DS}=-24V, V_{GS}=0V,$	I_{DSS}	--	--	-1	μA
Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	--	--	± 100	nA
Forward Transconductance	$V_{DS}=-10V, I_D=10A$	G_{FS}	--	25	--	S
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-10A$	$R_{DS(ON)}$	--	8	10	$m\Omega$
	$V_{GS}=-4.5V, I_D=-8A$		--	12	16	
Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	$V_{GS(th)}$	-1.0	--	-2.5	V
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Gate Resistance	$F=1MHz$	R_g	--	13	--	Ω
Total Gate Charge	$V_{DS}=-15V, V_{GS}=-10V, I_D=-10A$	Q_g	--	60	--	nC
Gate-Source Charge		Q_{gs}	--	8	--	
Gate-Drain Charge		Q_{gd}	--	13	--	
Turn-On Delay Time	$V_{DS}=-15V, V_{GS}=-10V, R_{GS}=3\Omega, I_D=-10A$	$T_{d(on)}$	--	12	--	ns
Rise Time		T_r	--	18	--	
Turn-Off Delay Time		$T_{d(off)}$	--	95	--	
Fall Time		T_f	--	110	--	
Input Capacitance	$V_{DS}=-15V, V_{GS}=0V, F=1MHz$	C_{iss}	--	3000	--	pF
Output Capacitance		C_{oss}	--	400	--	
Reverse Transfer Capacitance		C_{rss}	--	320	--	
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
Diode Forward Voltage	$V_{GS}=0V, I_S=-10A$	V_{SD}	--	-0.8	-1.2	V
Reverse Recovery Time	$I_F=-10A, dI_F/dt=100A/\mu s$	T_{rr}	--	18	--	ns
Reverse Recovery Charge		Q_{rr}	--	8.3	--	nC

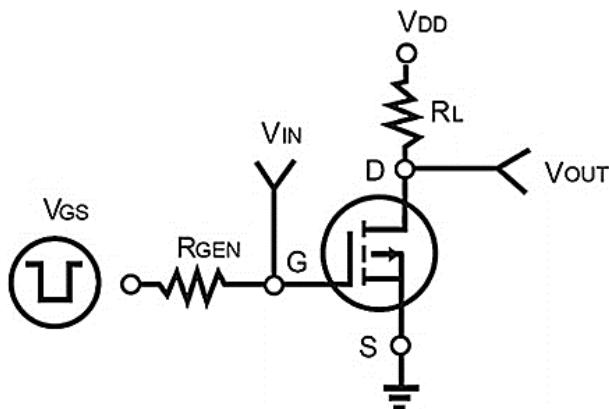
Note:

1. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ C$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of $R_{DS(on)}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. copper, in a still air environment with $T_A=25^\circ C$. The power dissipation P_D is based on $R_{DS(on)}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ C$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ C$.
4. Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. Essentially independent of operating temperature typical characteristics.

Switching Time Waveform



Switching Test Circuit



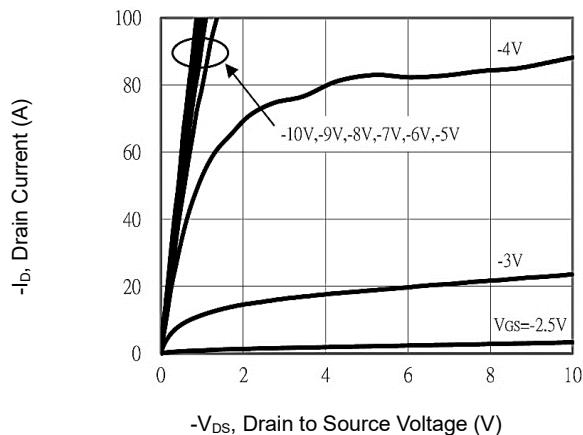
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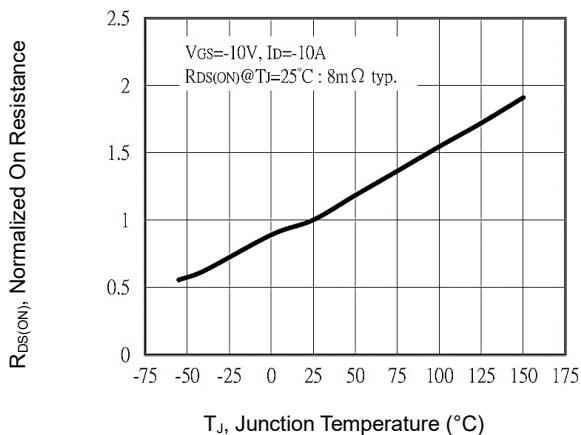
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CHARACTERISTIC CURVES

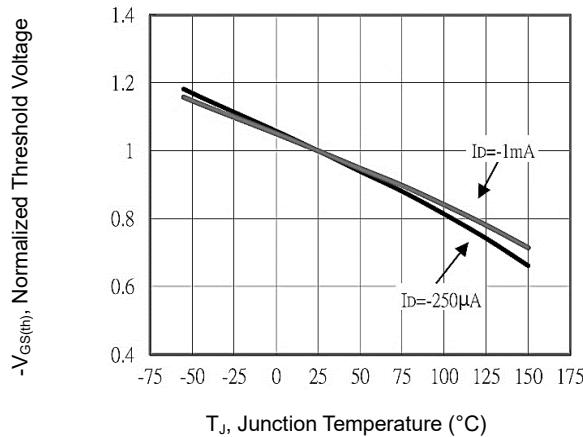
On-Region Characteristics



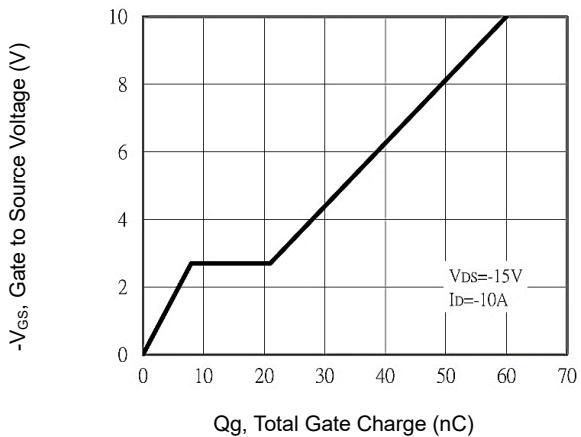
Normalized On-Resistance



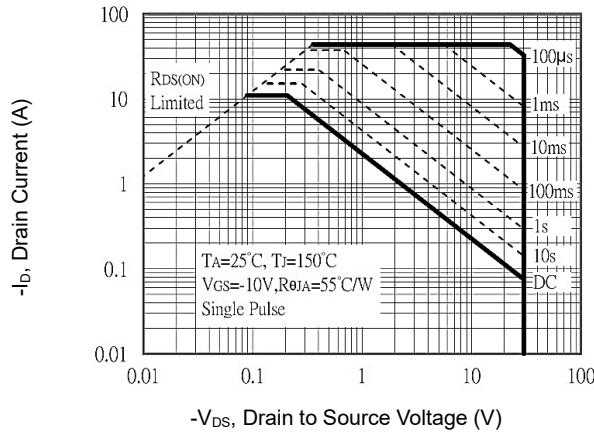
Threshold Voltage vs. Temperature



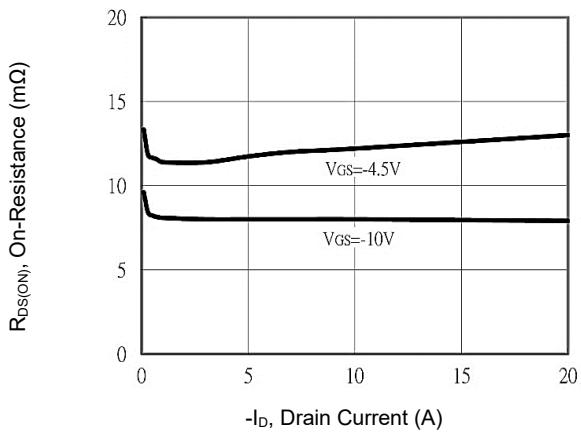
Gate Charge Characteristics



Maximum Safe Operating Area



On-Resistance vs. Drain Current



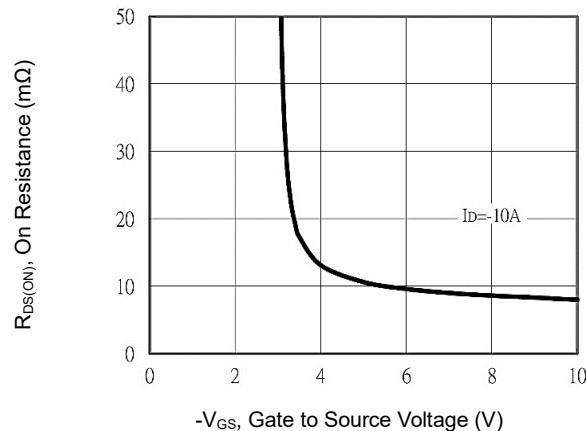
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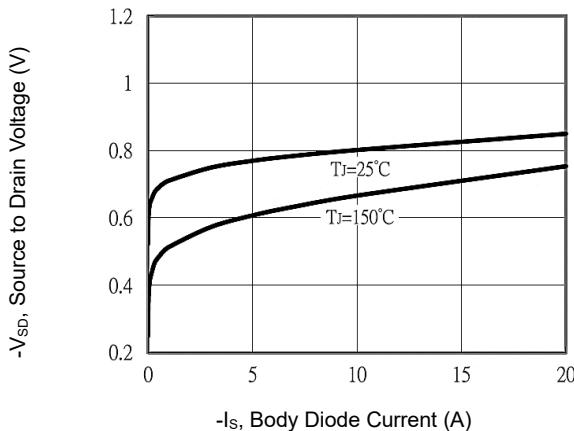
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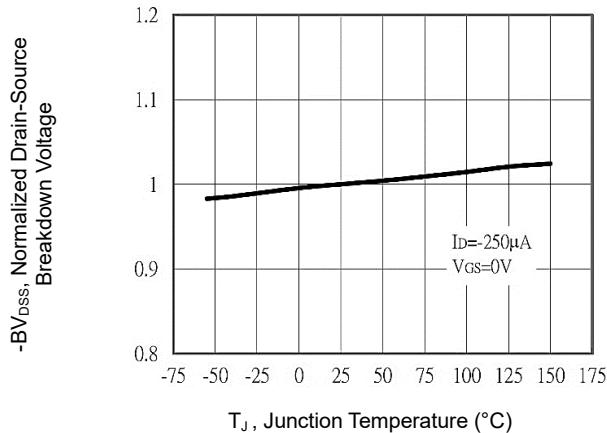
On-Resistance Variation with V_{GS}



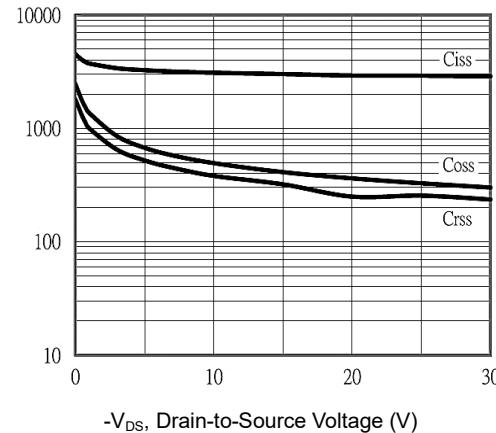
Body Diode Characteristics



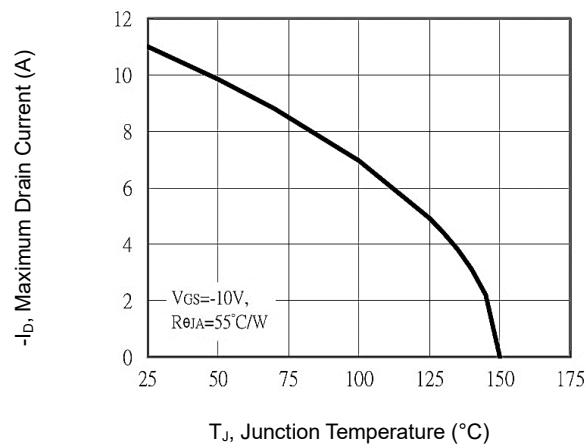
Breakdown Voltage vs. Junction Temperature



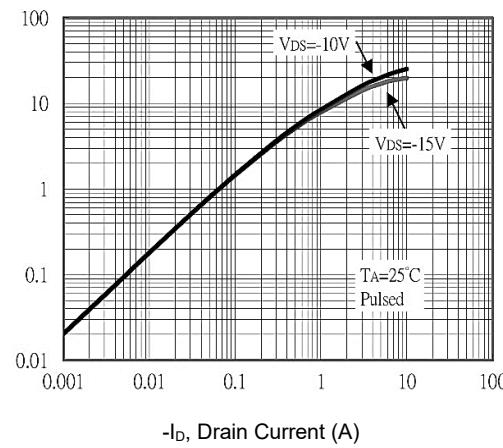
Capacitance vs. Drain-Source Voltage



Maximum Drain Current vs. Junction Temperature

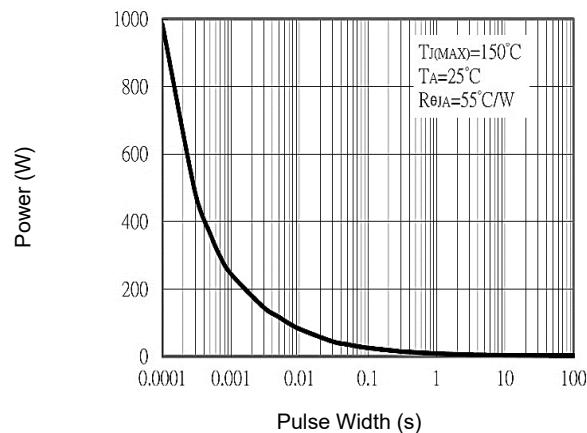


Forward Transfer Admittance vs. Drain Current

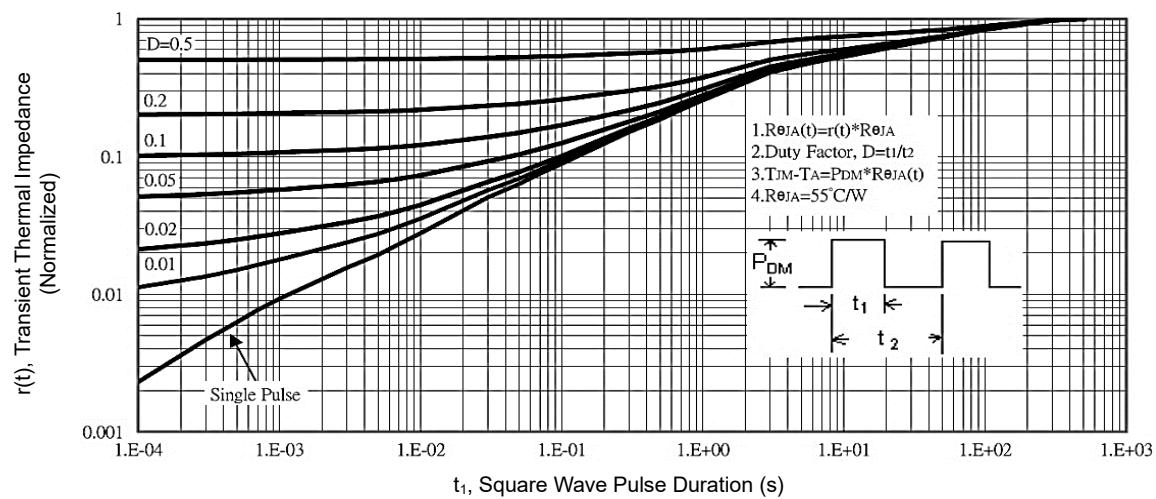


CHARACTERISTIC CURVES

Single Pulse Power Rating



Normalized Transient Thermal Impedance Curve



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DIMENSIONS AND RECOMMENDED LAND PATTERN

SOP-8	Min (mm)	Max (mm)
A1	0.10	0.25
A2	1.35	1.55
A3	1.35	1.75
b	0.33	0.51
c	0.17	0.25
D	4.70	5.10
E	5.80	6.20
E1	3.80	4.00
e	1.27	
L	0.40	1.27
X	0.60	
X1	3.81	
Y	1.52	
Y1	7.00	
C	1.27	

