FEATURE

- Magnetic Shield Construction for Power Circuit.
- Large Current and Low DC Resistance
- Low Profile Power Inductors
- Application: DC/DC Converter, Battery Powered Devices, Low Profile High Current Power Supply, Notebook/Server
- AEC-Q200 Compliant

ELECTRICAL CHARACTERISTICS



MERITEK

Part Number	Inductance	Tolerance	Test	DCR	(mΩ)	I _{SAT}	(A)	I _{RMS} (A)	
Part Number	(µH)	(%)	Frequency (Hz)	Тур.	Max.	Тур.	Max.	Тур.	Max.
PIWR33M3225CM65	0.33	±20%	1V/100K	19	22.8	4.50	4.20	5.0	4.5
PIWR47M3225CM65	0.47	±20%	1V/100K	25	30	4.00	3.80	4.5	4.1
PIWR68M3225CM65	0.68	±20%	1V/100K	32	38	3.70	3.40	4.1	3.6
PIW1R0M3225CM65	1.00	±20%	1V/100K	39	47	3.00	2.80	3.5	3.2
PIW1R5M3225CM65	1.50	±20%	1V/100K	48	58	2.40	2.20	3.2	3.0
PIW2R2M3225CM65	2.20	±20%	1V/100K	72	86	2.10	1.90	2.9	2.7
PIW3R3M3225CM65	3.30	±20%	1V/100K	105	126	1.80	1.60	2.5	2.2
PIW4R7M3225CM65	4.70	±20%	1V/100K	148	177	1.50	1.30	2.2	2.0
PIW5R6M3225CM65	5.60	±20%	1V/100K	170	204	1.25	1.15	1.9	1.7
PIW6R8M3225CM65	6.80	±20%	1V/100K	200	240	1.15	1.05	1.7	1.4
PIW8R2M3225CM65	8.20	±20%	1V/100K	260	312	1.00	0.90	1.5	1.3
PIW100M3225CM65	10.0	±20%	1V/100K	350	420	0.92	0.82	1.3	1.1
PIW150M3225CM65	15.0	±20%	1V/100K	460	552	0.70	0.65	1.0	0.9
PIW220M3225CM65	22.0	±20%	1V/100K	660	792	0.60	0.55	0.8	0.7

Notes:

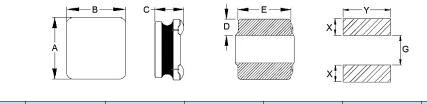
1. All test data referenced to 25°C ambient.

2. Saturation Current (Isat) based on inductance drop ($\Delta L/L0$: $\leq 30\%$) approximately

3. Heat Rated Current (Irms) based on temperature rise (Δ T: 40 °C) approximately

4. Operating Temperature: -55°C ~ +125°C (Including Self-temperature rise)

DIMENSIONS

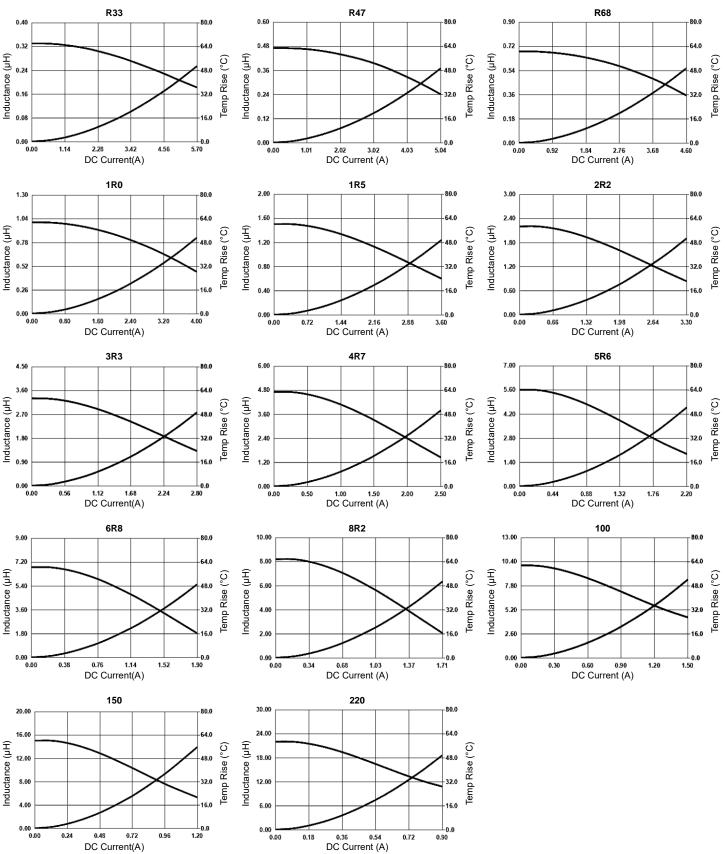


								Unit: mm
Size Code	Α	В	С	D	E	Х	Y	G
3225C	3.2 ±0.2	2.5 ±0.2	1.0 ±0.2	1.0 ±0.3	2.5 ±0.2	1.25	3.0	1.0

Notes: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.15mm and above.

MERITEK

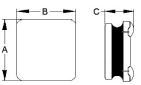
CHARACTERISTIC CURVES- PIW- 3225CM65 series

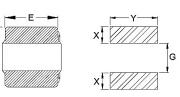


RELIABILITY TEST CONDITON AND REQUIREMENT

Item		Test Standar	ds / Condition	s / Equipment		Requirement				
Inductance	HP4284A, CH	11025, CH3302	2, CH1320, CH	1320S, LCR M	eter	Refer to specification				
DC Resistance		lent33420A Mic		,	-	Refer to specification				
	GITTOJUZ, AGI		1	1						
	T	Peak	Normal	Wave	Velocity	Appearance: No damage				
Mechanical	Туре	value	duration	form	change	Inductance: within $\pm 10\%$ of initial value				
Shock	SMD	(g's) 100	(D) (ms) 6	Half-sine	(Vi) ft/sec 12.3	Q: Shall not exceed the specification value				
		100	6	Half-sine	12.3	RDC: within ±15% of initial value and shal not exceed the specification value				
	Lead	100	0	nall-sine	12.3	not exceed the specification value				
		Hrs at 155°C d		C±5°C		Mana there OF9(of the terrein shall also territe				
Solderability		0/-0.5 seconds egory 3. (steam		15min) at 260°	0.4500	More than 95% of the terminal electrode should be covered with solder.				
	Test Time: 30+	-0/-0.5 seconds	aying o nours <u>.</u> S.	L TOTTITT) at 200	013 0	Should be covered with solder.				
	Solder temper	ature: 260±5°C	for 10 seconds	5						
Resistance to		amp/immersior			6 mm/s.	Annone No. 1.				
Soldering Heat		ver the termina				Appearance: No damage Inductance: within ±10% of initial value				
	Oscillation Fre	equency: 10~2	K \sim 10 Hz for 2	0 minutes		Q: Shall not exceed the specification value				
Vibration		ibration checke				RDC: within ±15% of initial value and shal not exceed the specification value				
VIDIALIUII		e:1.52mm ± 10	not exceed the specification value							
	Testing Time:	12 hours (20 m	inutes, 12 cycle							
High	Temperature:									
Temperature	Duration 1000	Hrs Min								
Exposure	Measured at r	oom temperatu	re after placing	for 24±2hrs		Appearance: No damage				
Biased		3% R.H. Tempe	erature: 85°C±2	°C		Inductance: within ±10% of initial value				
Humidity	Duration: 1000			Q: Shall not exceed the specification value						
inannanty	Measured at F	Room Temperat	ure after placin	RDC: within ±15% of initial value and shall						
High	Temperature:	125±2°C				not exceed the specification value				
Temperature)Hrs Min. with '	00% rated cur							
Operational Life	Measured at F	Room Temperat	ure after placin	g for 24±2Hrs						
	Condition for 2	cycle								
	Step	1	Appearance: No damage							
Temperature	Temperature		125 ±2°C	125 ±2°C	Low Temp	 Inductance: within ±10% of initial value Q: Shall not exceed the specification valu RDC: within ±15% of initial value and sha 				
Cycling	Duration	30min Min	1 min Max	30 min Min	1 min Max					
	Number of Cy		not exceed the specification value							
		oom temperatu	re alter placing	for 24±2nrs						
	Condition for 2					Appearance: No damage				
	Step	-55 ±2°C	405	2 5 ±2°C	3 125 ±2°C	Inductance: within ±10% of initial value				
Thermal Shock	Temperature Duration	-55 ±2 C) ±2 C)sec	125 ±2 C 15±1min	Q: Shall not exceed the specification value				
	Number of cyc		20	RDC: within ±15% of initial value and shal						
		oom temperatu	not exceed the specification value							
ESD		2 HBM ESD, C	1 0		l evel 2)	Appearance: No damage				
Resistance to										
Solvents		wash chemical		or equivalent.		Appearance : No damage				
		ounted on a PC								
Terminal		the side of a de rce shall be ap		N DUT	Wide					
		the force shall			Thick	Appearance : No damage				
				Substrate	Press tool					
	gradually as n									
	gradually as n being tested.			Place the 100x40mm board into a fixture						
	being tested. Place the 100	x40mm board i		Support Solder	Drip Printed circuit based before lesting					
Strength	being tested. Place the 100 with the comp	x40mm board in onent facing do	wn.	Support Solder	Chip Printed circuit bandt before lesting					
Strength	being tested.Place the 100with the compApply a force	x40mm board in onent facing do which will bend	wn. the board	Support Solidar	Crip Printed circuitbased before lesing	Appearance : No damage				
	being tested. Place the 100 with the comp Apply a force (D) x = 2mm n	x40mm board in onent facing do which will bend ninimum. Durat	wn. the board ion: 60 (+5)	5,0000 Sakty	Drip Proted claust ballere leading	Appearance : No damage				
Strength	being tested. Place the 100 with the comp Apply a force (D) x = 2mm n	x40mm board in onent facing do which will bend hinimum. Durat Force is to be a	wn. the board ion: 60 (+5)	545005 54547 0 012 14 14 14 14 14	Drp Proto circuit bards before teoring	Appearance : No damage				
Strength	being tested. Place the 100 with the comp Apply a force (D) x = 2mm n seconds. The	x40mm board in onent facing do which will bend hinimum. Durat Force is to be a	wn. the board ion: 60 (+5)	Support SASer 412 Radius 300 Private result source two	Press could based balance basing	Appearance : No damage				

DIMENSIONS- PIW-M65 series





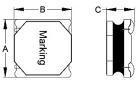
PIW-3225CM65

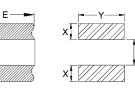
								Unit: mm
Size Code	Α	В	С	D	E	Х	Y	G
2016A	2.0 ±0.2	1.6 ±0.2	0.7 ±0.1	0.7 ±0.3	1.8 ±0.2	1.0	2.1	0.5
2016B	2.0 ±0.2	1.6 ±0.2	0.9 ±0.1	0.7 ±0.3	1.6 ±0.2	1.0	2.1	0.5
2016C	2.0 ±0.2	1.6 ±0.2	1.0 ±0.2	0.7 ±0.3	1.6 ±0.2	1.0	2.1	0.5
2520A	2.5 ±0.2	2.0 ±0.2	0.7 ±0.1	0.9 ±0.3	2.0 ±0.2	1.15	2.5	0.7
2520B	2.5 ±0.2	2.0 ±0.2	0.9 ±0.1	0.9 ±0.3	2.0 ±0.2	1.15	2.5	0.7
2520C	2.5 ±0.2	2.0 ±0.2	1.0 ±0.2	0.9 ±0.3	2.0 ±0.2	1.15	2.5	0.7
3225C	3.2 ±0.2	2.5 ±0.2	1.0 ±0.2	1.0 ±0.3	2.5 ±0.2	1.25	3.0	1.0

D

D

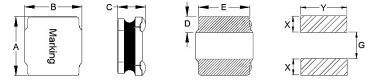
Notes: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.15mm and above.





								Unit: mm
Size Code	Α	В	С	D	E	Х	Y	G
3612	3.6 ±0.2	3.6 ±0.2	1.0 ±0.2	1.2 ±0.3	3.2 ±0.3	0.9	3.7	2.0
4010	4.0 ±0.2	4.0 ±0.2	0.9 ±0.1	1.2 ±0.3	3.5 ±0.3	1.5	4.5	1.5
4012	4.0 ±0.2	4.0 ±0.2	1.0 ±0.2	1.2 ±0.3	3.5 ±0.3	1.5	4.5	1.5
4018	4.0 ±0.2	4.0 ±0.2	1.6 ±0.2	1.1 ±0.2	3.5 ±0.3	1.5	4.5	1.5
4020	4.0 ±0.2	4.0 ±0.2	1.8 ±0.2	1.2 ±0.3	3.4 ±0.3	1.5	4.5	1.5
4030	4.0 ±0.2	4.0 ±0.2	3.0 Max.	1.35 ±0.3	3.4 ±0.4	1.5	3.7	1.3
5010	5.0 ±0.2	5.0 ±0.2	0.9 ±0.1	1.5 ±0.3	4.0 ±0.3	1.85	5.5	1.8
5012	5.0 ±0.2	5.0 ±0.2	1.0 ±0.2	1.5 ±0.3	4.0 ±0.3	1.85	5.5	1.8
5020	5.0 ±0.2	5.0 ±0.2	1.8 ±0.2	1.3 ±0.2	4.7 ±0.2	1.5	4.7	2.1
5030	5.0 ±0.2	5.0 ±0.2	2.8 ±0.2	1.3 ±0.2	4.7 ±0.3	1.85	5.5	1.8
5040 (≤ 10 µH)	4.95 ±0.2	4.95 ±0.2	3.9 ±0.2	1.3 ±0.2	4.2 ±0.2	1.5	4.2	2.1
5040 (> 10 µH)	4.95 ±0.2	4.95 ±0.2	3.8 ±0.2	1.3 ±0.2	4.2 ±0.2	1.5	4.2	2.1
6020	6.0 ±0.2	6.0 ±0.2	1.8 ±0.2	1.6 ±0.3	5.8 ±0.3	1.8	5.8	2.5
6028	6.0 ±0.2	6.0 ±0.2	2.6 ±0.2	1.6 ±0.3	5.8 ±0.3	1.8	5.8	2.5

Notes: 1. The above PCB layout reference only. 2. Recommend solder paste thickness at 0.15mm and above.



								Unit: mn
Size Code	Α	В	С	D	E	Х	Y	G
3010	3.0 ±0.2	3.0 ±0.2	0.9 ±0.1	0.9 ±0.3	2.7 ±0.3	1.25	3.5	0.9
3012	3.0 ±0.2	3.0 ±0.2	1.0 ±0.2	0.9 ±0.3	2.7 ±0.3	1.25	3.5	0.9
3015	3.0 ±0.2	3.0 ±0.2	1.3 ±0.2	0.9 ±0.3	2.7 ±0.3	1.25	3.5	0.9
6045	6.0 ±0.3	6.0 ±0.3	4.2 ±0.3	1.9 ±0.3	4.8 ±0.3	3.0	6.3	5.5
8040 (< 1.0 μH)	8.0 ±0.3	8.0 ±0.3	4.2 Max	2.4 ±0.3	6.3 ±0.3	2.85	6.6	2.8
8040 (≥ 1.0 µH)	8.0 ±0.3	8.0 ±0.3	3.7 ±0.3	2.4 ±0.3	6.3 ±0.3	2.85	6.6	2.8

Meritek Electronics Corporation | www.meritekusa.com

PART NUMBERING SYSTEM

<u>PIW</u> (1)	<u>220M</u> <u>322</u> (2) (3		<u>5</u>			
No	Item	Code	Description			
(1)	Product Code	PIW	Power Inductor series,	Wire Wound type		
(2)	(2) Inductance 220M 22µ		22µH ±20%(M)	First two digits: significant, Third: multiplier		
(3)	Size Code	3225C	3.2x2.5x1.0mm	Length x Width x Thickness (mm)		
(4)	Series Code	M65	Surface Mount Shielded, Low Profile, High Current series, AEC-Q200 Compliant			

RECOMMENDED SOLDERING PROFILES

Reflow Condition						
	Temp. Min T _{s(min)}	150°C				
Pre Heat	Temp. Max T _{s(max)}	200°C				
nout	Time (min. to max.) (t_s)	60 ~120 seconds				
-	ramp up rate (Liquidus ture) (T∟) to peak	3°C/second max				
T _{S(max)} to	T _L (Ramp-up rate)	3°C/second max				
Reflow	Temp. (T _∟)	217°C				
Reliow	Time (min. to max.) (t∟)	60 ~150 seconds				
Peak Ten	nperature (T _P)	See table below				
Time with Tempera	nin 5°C of actual peak ture (t _p)	10 seconds max				
Ramp-do	wn Rate	6°C/second max				
Reflow T	imes	3 times max				

Peak Temperature (T _P)								
Volume	< 350mm ³	350-2000mm ³	> 2000mm ³					
Thickness < 1.6mm	260°C	260°C	260°C					
Thickness 1.6-2.5mm	260°C	250°C	245°C					
Thickness ≥ 2.5mm	250°C	245°C	245°C					

*Specifications subject to change without notice

