

# N-Channel MOSFET

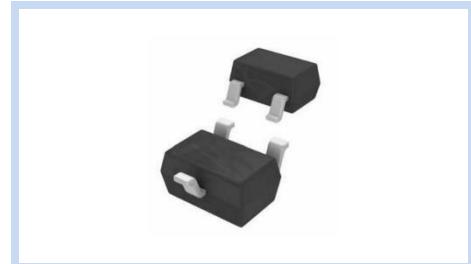
## 30V 300mA 350mW SOT-323 ESD

MFT3NA30S323E

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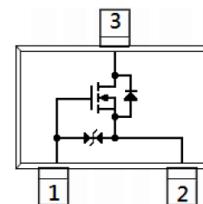
### FEATURE

- $R_{DS(ON)} < 1.2\Omega$ ,  $V_{GS} = 4.5V$ ,  $I_D = 300mA$
- $R_{DS(ON)} < 1.6\Omega$ ,  $V_{GS} = 2.5V$ ,  $I_D = 200mA$
- $R_{DS(ON)} < 2\Omega$ ,  $V_{GS} = 1.8V$ ,  $I_D = 100mA$
- Advanced Trench Process technology
- Application: Relay drive Systems, Speed line drive, etc
- ESD Protected



### MECHANICAL DATA

- Case: SOT-323 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

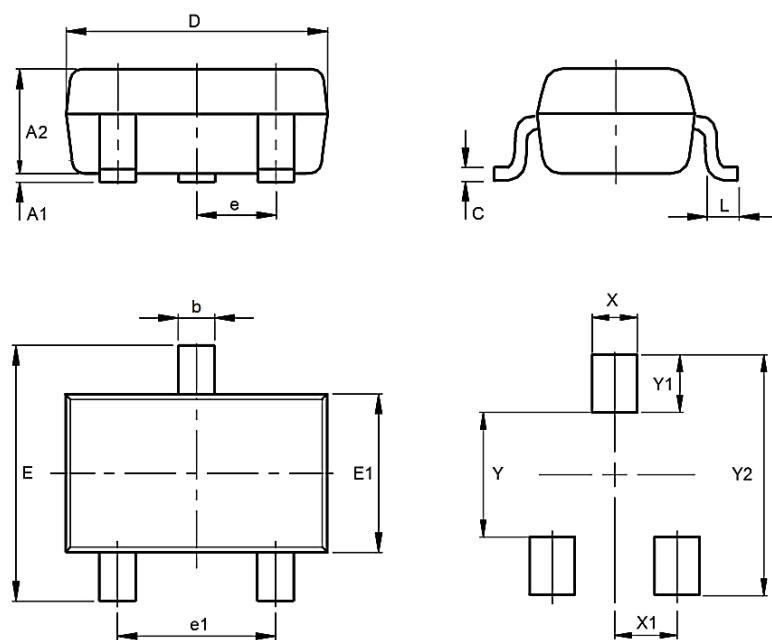


### MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 10$	V
Drain Current – Continuous		$I_D$	300	mA
Drain Current – Pulsed		$I_{DM}$	600	mA
Power Dissipation	$T_A=25^\circ C$	$P_D$	350	mW
	Derate above $25^\circ C$		2.8	$mW/^\circ C$
Operating Junction Temperature and Storage Temperature		$T_J, T_{stg}$	-55 to 150	°C
Thermal Resistance, Junction to Ambient		$R_{\theta JA}$	357	°C / W

### DIMENSIONS

Item	Min (mm)	Max (mm)
A1	--	0.10
A2	0.90	1.10
b	0.20	0.40
C	0.05	0.15
D	1.80	2.20
e	0.60	0.70
e1	1.20	1.40
E	2.00	2.20
E1	1.15	1.35
L	--	0.15
X	0.66	
X1	0.65	
Y	0.99	
Y1	0.86	
Y2	1.85	



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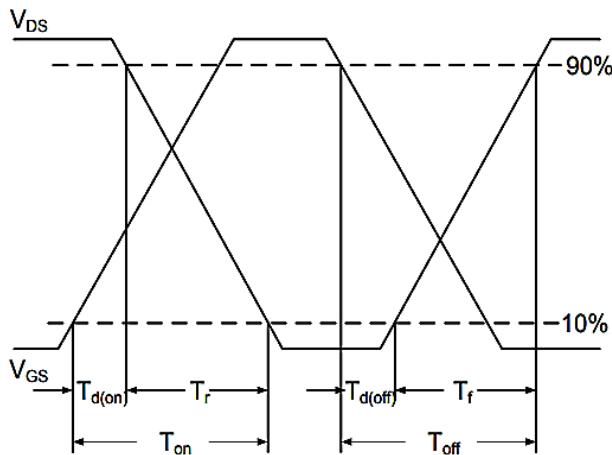
## ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Drain-Source Breakdown Voltage</b>	$V_{GS}=0V, I_D=250\mu A$	$BV_{DSS}$	30	--	--	V
<b>Gate Threshold Voltage</b>	$V_{GS}=V_{DS}, I_D=250\mu A$	$V_{GS(th)}$	0.4	0.75	1.0	V
<b>Gate Leakage Current</b>	$V_{DS}=0V, V_{GS}=\pm 8V$	$I_{GSS}$	--	--	$\pm 10$	$\mu A$
<b>Zero Gate Voltage Drain Current</b>	$V_{DS}=24V, V_{GS}=0V$	$I_{DS(0)}$	--	--	1	$\mu A$
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
<b>Drain-Source On-Resistance</b>	$V_{GS}=4.5V, I_D=300mA$	$R_{DS(on)}$	--	0.7	1.2	$\Omega$
	$V_{GS}=2.5V, I_D=200mA$		--	0.8	1.6	$\Omega$
	$V_{GS}=1.8V, I_D=100mA$		--	0.9	2	$\Omega$
	$V_{GS}=1.5V, I_D=50mA$		--	1.1	3	$\Omega$
	$V_{GS}=1.2V, I_D=20mA$		--	1.5	4	$\Omega$
Dynamic Characteristics	Conditions	Symbol	--	Typ.	Max	Unit
<b>Input Capacitance</b>	$V_{DS}=10V, V_{GS}=0V$ $F=1.0MHz$	$C_{iss}$	--	24	--	pF
<b>Output Capacitance</b>		$C_{oss}$	--	13	--	pF
<b>Reverse Transfer Capacitance</b>		$C_{rss}$	--	8	--	pF
<b>Turn-On Delay Time</b>	$V_{DS}=10V, I_D=300mA,$ $V_{GS}=4V, R_G=10\Omega$	$T_{d(on)}$	--	8.3	--	nS
<b>Rise Time</b>		$T_r$	--	5.7	--	nS
<b>Turn-Off Delay Time</b>		$T_{d(off)}$	--	35	--	nS
<b>Fall Time</b>		$T_f$	--	12	--	nS
<b>Total Gate Charge</b>		$Q_g$	--	0.9	--	nC
<b>Gate-Source Charge</b>	$V_{DS}=10V, V_{GS}=4.5V,$ $I_D=300mA$	$Q_{gs}$	--	0.3	--	nC
<b>Gate-Drain Charge</b>		$Q_{gd}$	--	0.2	--	nC
<b>Drain-Source Body Diode</b>		<b>Conditions</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ.</b>	<b>Unit</b>
<b>Diode Forward Voltage</b>	$I_S=300mA, V_{GS}=0V$	$V_{SD}$	--	0.9	1.3	V
<b>Diode Foward Current</b>	---	$I_S$	--	--	300	mA

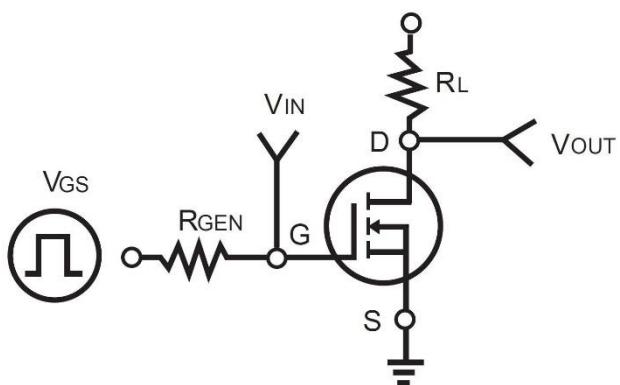
Note:

1. Essentially independent of operating temperature typical characteristics.
2. Pulse width<300 $\mu s$ , Duty cycle<2%.
3.  $R_{QJA}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz square pad of copper.
4. Guaranteed by design, not subject to production testing.
5. The maximum current rating is package limited.

Switching Time Waveform

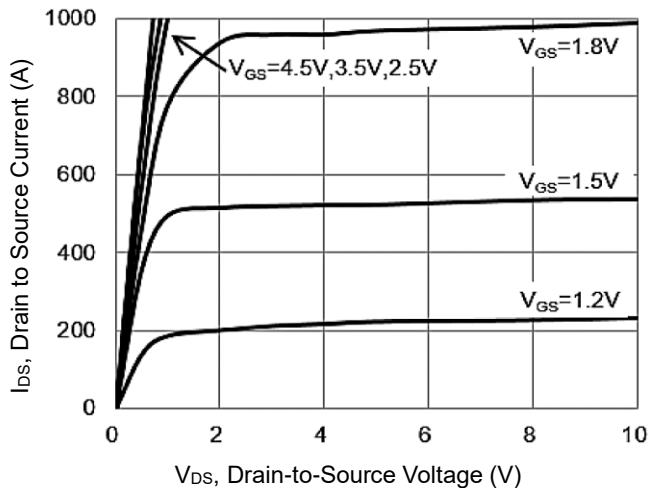


Switching Test Circuit

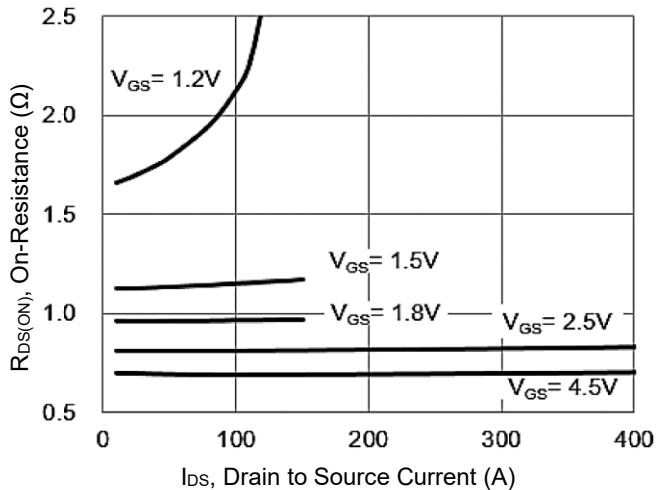


## CHARACTERISTIC CURVES

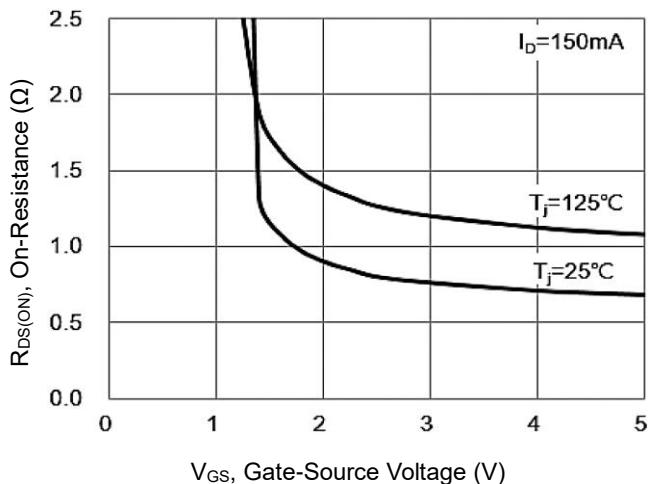
### On Region Characteristics



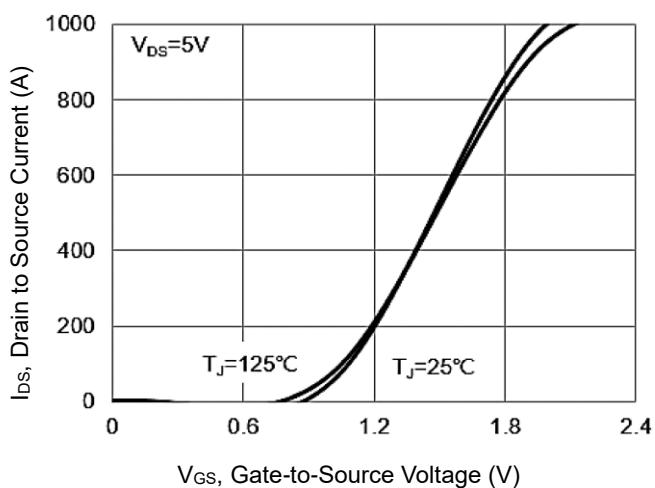
### On-Resistance vs. Drain Current



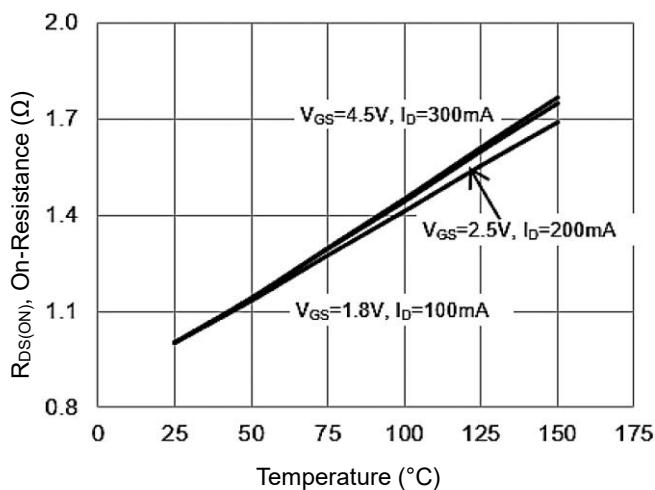
### On-Resistance Variation with $V_{GS}$



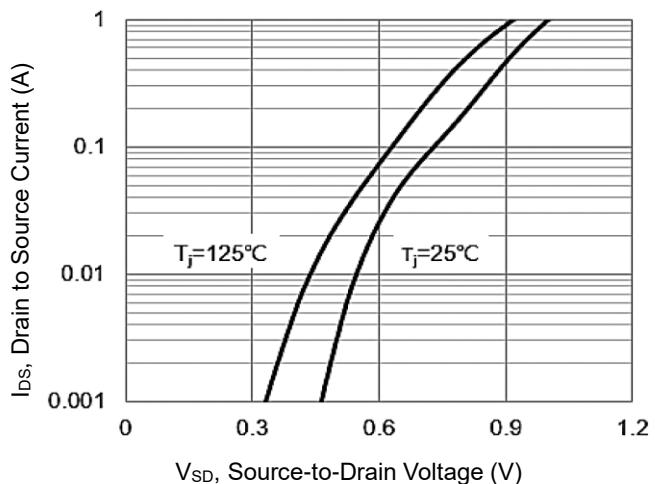
### Transfer Characteristics



### On-Resistance vs. Junction Temperature



### Body Diode Characteristics



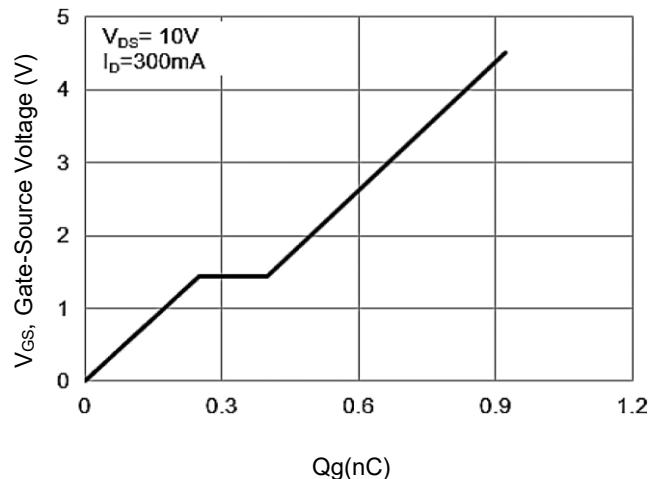
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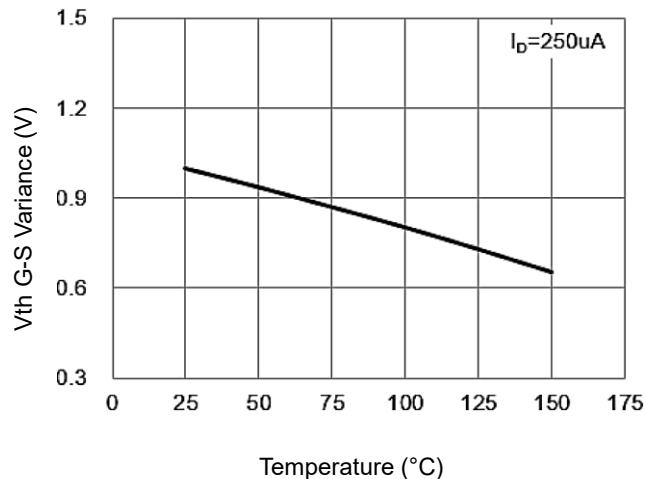
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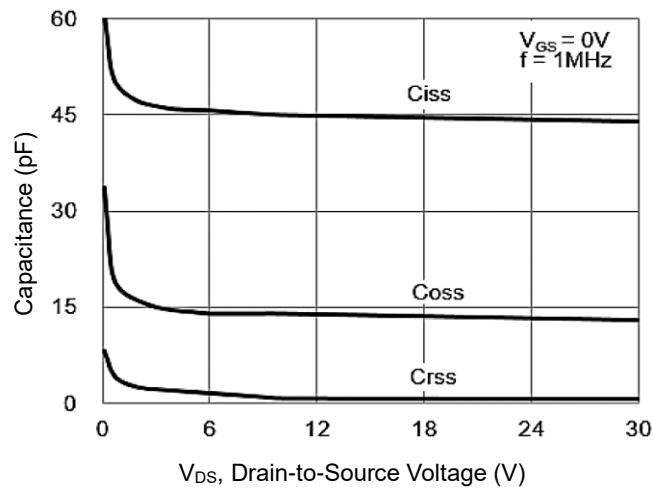
Gate Charge Characteristics



Threshold Voltage Variance vs. Temperature



Capacitance vs. Drain-Source Voltage



Breakdown Voltage vs Junction Temperature

