

# N-Channel MOSFET

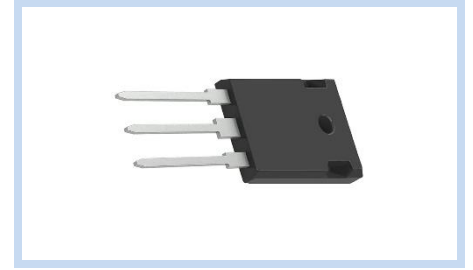
## 650V 39A 357W TO-247

MFT70N39AT247

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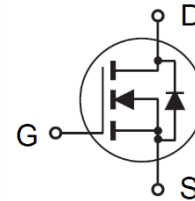
### FEATURE

- $R_{DS(ON)}=0.095\Omega$  at  $V_{GS}=10V$ ,  $I_D=39A$
- High Power and Current Handling Capability
- Super High Dense Cell Design for Extremely Low  $R_{DS(ON)}$
- Lead free in compliance with EU RoHS 2.0



### MECHANICAL DATA

- Case: TO-247 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

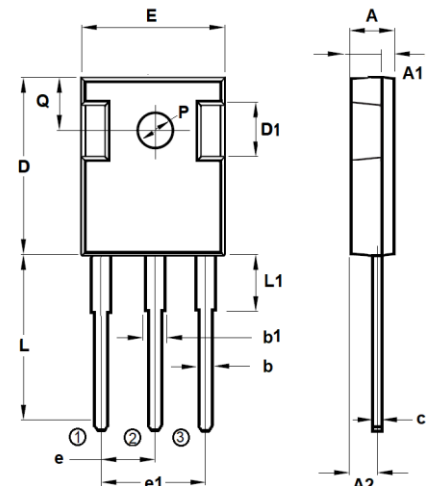


### MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Drain Current – Continuous	$I_D$	$T_C=25^\circ C$	39
		$T_C=100^\circ C$	24.5
Drain Current – Pulsed	$I_{DM}$	156	A
Power Dissipation	$P_D$	$T_C=25^\circ C$	357
		Derate above $25^\circ C$	2.9
Single Pulsed Avalanche Energy	$E_{AS}$	735	mJ
Single Pulsed Avalanche Current	$I_{AS}$	7	A
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62.5	$^\circ C/W$
Thermal Resistance, Junction to Case	$R_{\theta JC}$	0.35	$^\circ C/W$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

### DIMENSIONS

Item	Min (mm)	Max (mm)
A	4.70	5.31
A1	1.5	2.49
b	0.99	1.40
b1	1.65	2.39
c	0.38	0.89
D	20.3	21.46
D1	4.32	5.49
E	15.45	16.26
e	5.45	--
E1	10.9	--
L	19.81	20.57
L1	--	4.50
P	3.50	3.70
Q	5.38	6.20



Note: Pin Layout: 1 :Gate, 2: Drain, 3: Source

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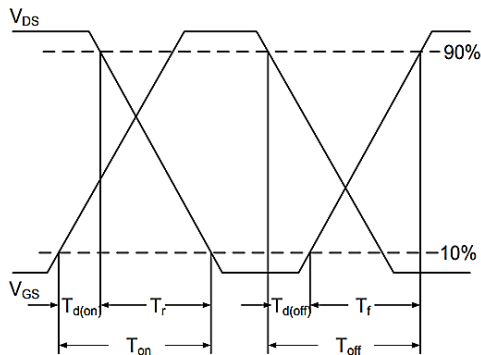
### ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	$BV_{DSS}$	650	--	--	V
Zero Gate Voltage Drain Current	$V_{DS}=650V, V_{GS}=0V$	$I_{DSS}$	--	--	1	$\mu A$
Gate-Body Leakage Current, Forward	$V_{GS}=30V, V_{DS}=0V$	$I_{GSSF}$	--	--	100	nA
Gate-Body Leakage Current, Reverse	$V_{GS}=-30V, V_{DS}=0V$	$I_{GSSR}$	--	--	-100	nA
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$	$R_{DS(ON)}$	--	0.08	0.095	$\Omega$
Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	$V_{GS(th)}$	2.5	--	4.5	V
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Total Gate Charge	$V_{DS}=520V, V_{GS}=10V, I_D=20A$	$Q_g$	--	69	--	nC
Gate-Source Charge		$Q_{gs}$	--	12	--	nC
Gate-Drain Charge		$Q_{gd}$	--	30	--	nC
Turn-On Delay Time	$V_{DD}=520V, V_{GS}=10V, R_G=6\Omega, I_D=20A$	$T_{d(on)}$	--	37	--	ns
Rise Time		$T_r$	--	17	--	ns
Turn-Off Delay Time		$T_{d(off)}$	--	95	--	ns
Fall Time		$T_f$	--	9	--	ns
Input Capacitance		$C_{iss}$	--	1915	--	pF
Output Capacitance	$V_{DS}=150V, V_{GS}=0V, F=1MHz$	$C_{oss}$	--	110	--	pF
Reverse Transfer Capacitance		$C_{rss}$	--	5	--	pF
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Diode Forward Current	--	$I_S$	--	--	39	A
Diode Forward Voltage	$V_{GS}=0V, I_S=20A, T_J=25^\circ C$	$V_{SD}$	--	--	1.5	V
Reverse Recovery Time	$V_R=25V, I_F=10A, di_F/dt=100A/us$	$T_{rr}$	--	324	--	ns
Reverse Recovery Charge		$Q_{rr}$	--	4.2	--	$\mu C$

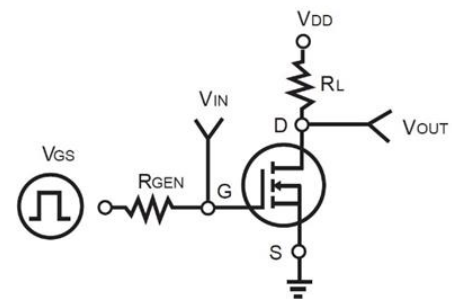
**Note:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$
3. Guaranteed by design, not subject to production testing.
4.  $L=30mH, I_{AS} = 7A, V_{DD}= 60V, R_G=25\Omega$ , Starting  $T_J=25^\circ C$

Switching Time Waveform



Switching Test Circuit



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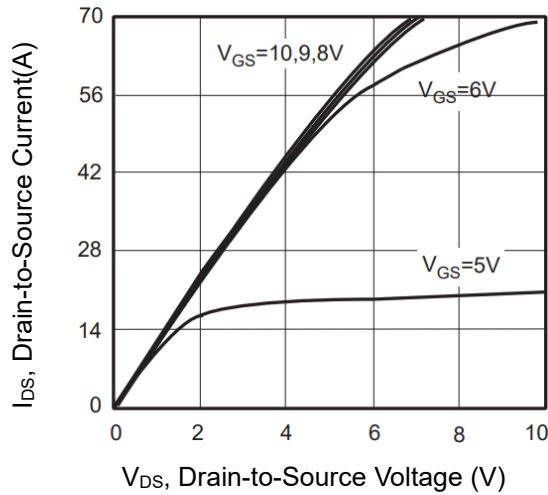
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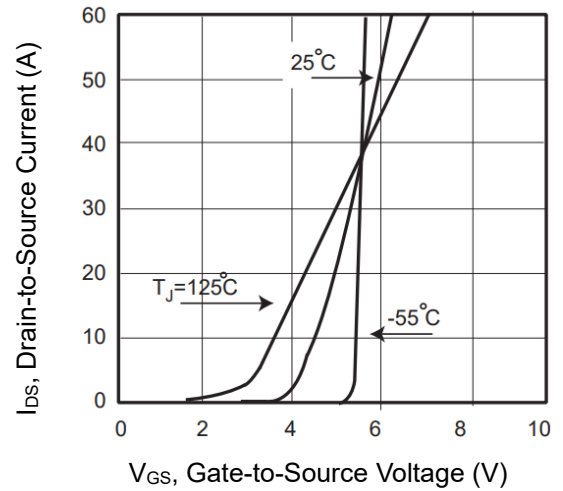
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### CHARACTERISTIC CURVES

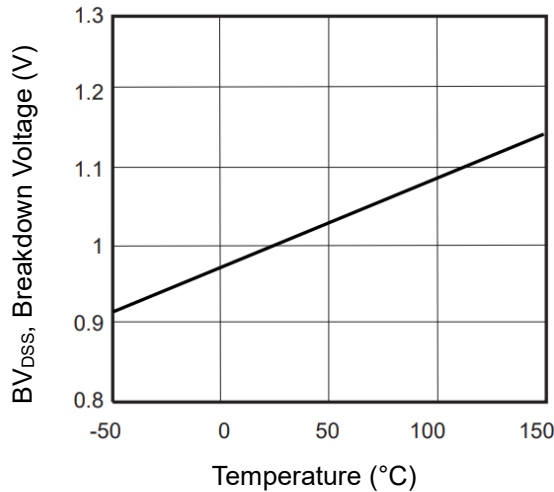
Output Characteristics



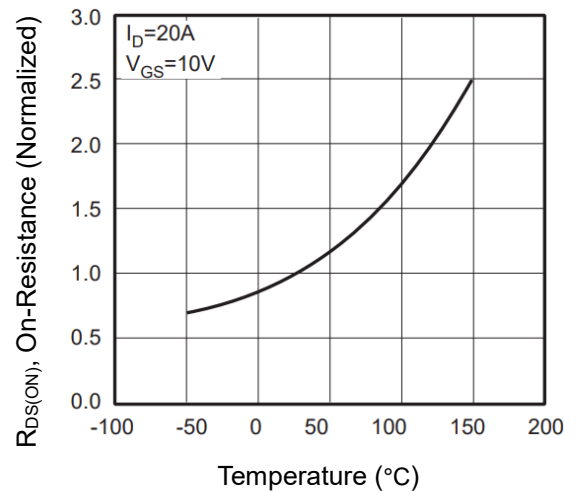
Transfer Characteristics



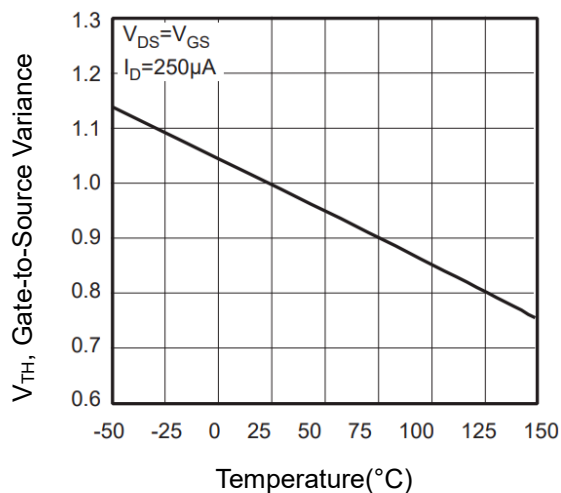
Breakdown Voltage Variation vs. Temperature



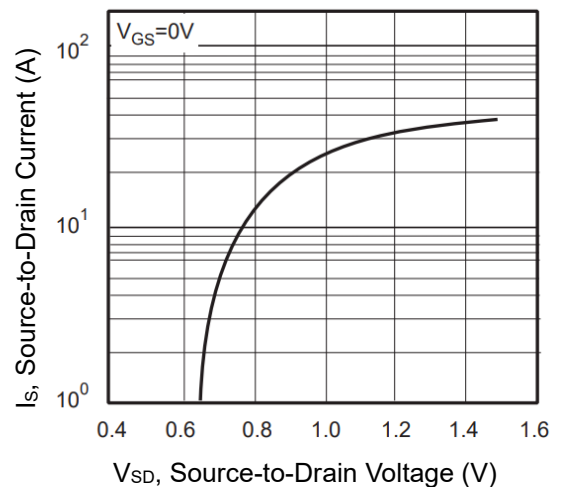
On-Resistance vs. Junction temperature



Threshold Voltage vs. Temperature



Body Diode Characteristics



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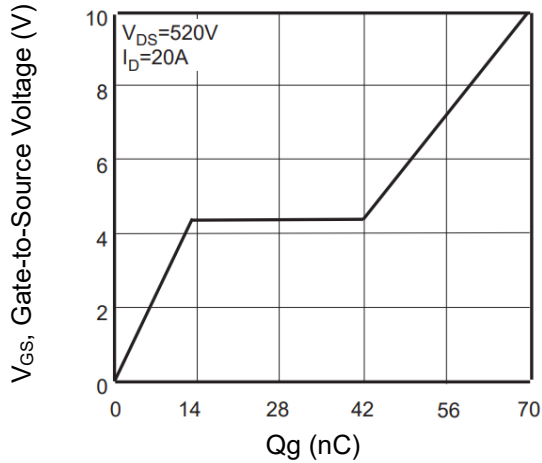
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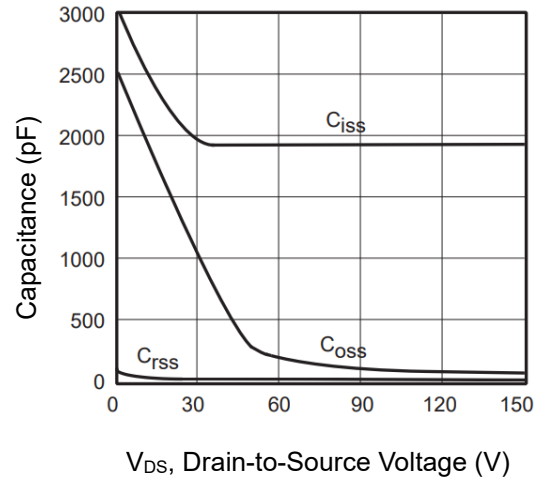
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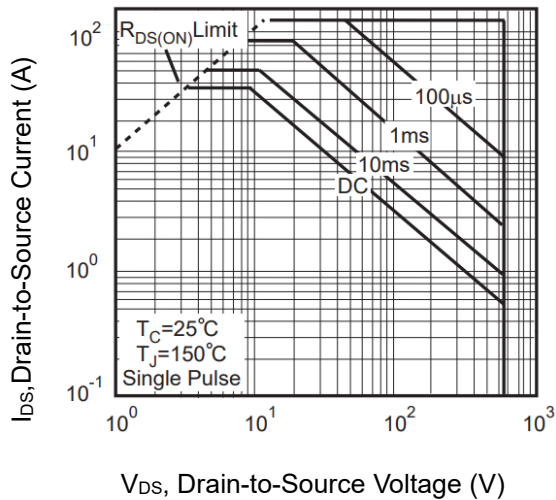
Gate-Charge Characteristics



Capacitance vs. Drain-Source Voltage



Maximum Safe Operating Area



Normalized Transient Thermal Impedance Curve

