

N-Channel MOSFET

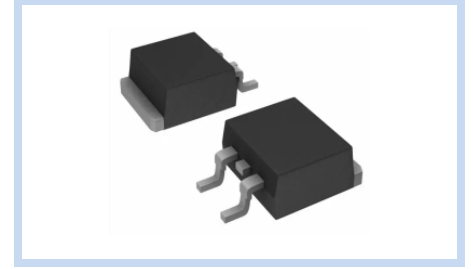
900V 2A TO-252

MFT90N2A0T252

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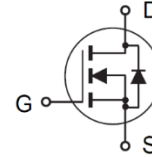
FEATURE

- $R_{DS(ON)} < 6.8\Omega$ at $V_{GS}=10V$, $I_D=1A$
- High Power and Current Handling Capability
- Super High Dense Cell Design for Extremely Low $R_{DS(ON)}$



MECHANICAL DATA

- Case: TO-252 Package
- Terminals: Solderable per MIL-STD-750, Method 2026

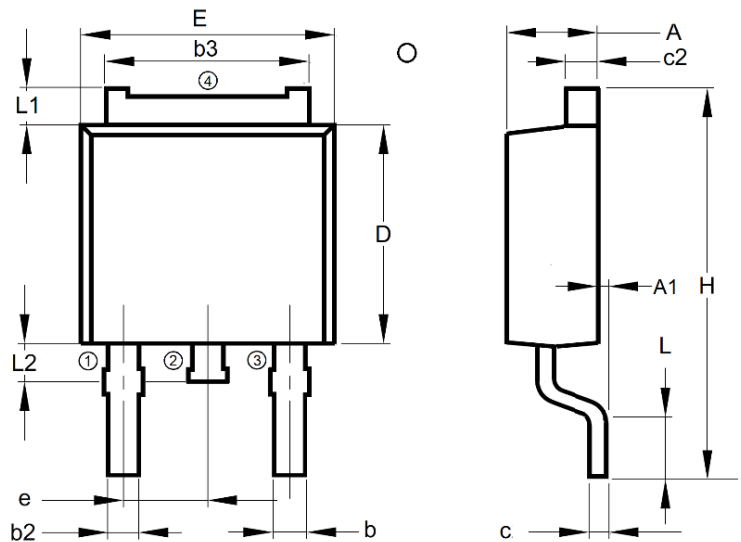


MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	900	V
Gate-Source Voltage		V_{GS}	± 30	V
Drain Current – Continuous	$T_C=25^\circ C$	I_D	2	A
Drain Current – Pulsed	$T_C=25^\circ C$	I_{DM}	8	A
Power Dissipation	$T_C=25^\circ C$	P_D	75	W
	Derate above $25^\circ C$		0.5	W/ $^\circ C$
Thermal Resistance Junction to Ambient		$R_{\theta JA}$	50	$^\circ C/W$
Thermal Resistance Junction to Case		$R_{\theta JC}$	2	$^\circ C/W$
Operating Junction and Storage Temperature		T_J, T_{STG}	-55 to 175	$^\circ C$

DIMENSIONS

Item	Min. (mm)	Max. (mm)
A	2.20	2.40
A1	--	0.13
b	0.50	0.90
b2	0.76	1.14
b3	4.95	5.59
c	0.40	0.61
c2	0.45	0.89
D	5.40	6.63
E	6.05	7.10
e	1.98	2.59
H	8.80	10.60
L	0.25	--
L1	0.70	1.78
L2	0.50	1.20



Note: 1: Gate, 2, 4: Drain, 3: Source

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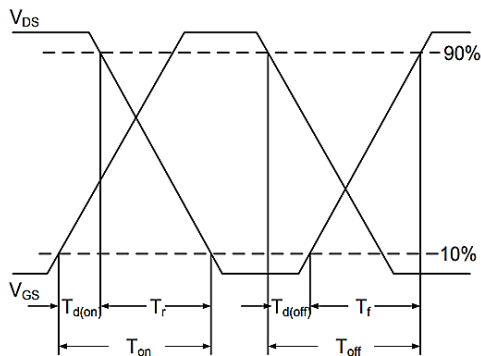
ELECTRICAL CHARACTERISTICS

Off Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	BV_{DSS}	900	-	-	V
Drain-Source Leakage Current	$V_{DS}=900V, V_{GS}=0V$	I_{DSS}	-	-	25	μA
Gate-Body Leakage Current, Forward	$V_{GS}=30V, V_{DS}=0V$	I_{GSSF}	-	-	100	nA
Gate-Body Leakage Current, Reverse	$V_{GS}=-30V, V_{DS}=0V$	I_{GSSR}	-	-	-100	nA
On Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=1A$	$R_{DS(ON)}$	-	5.3	6.8	Ω
Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	$V_{GS(th)}$	2	-	4	V
Dynamic Characteristics	Conditions	Symbol	Min	Typ.	Max	Unit
Total Gate Charge	$V_{DS}=720V, V_{GS}=10V, I_D=2A$	Q_g	-	22	-	nC
Gate-Source Charge		Q_{gs}	-	4	-	
Gate-Drain Charge		Q_{gd}	-	12	-	
Turn-On Delay Time	$V_{DD}=450V, V_{GS}=10V, R_G=25\Omega, I_D=2A$	$T_{d(on)}$	-	27	-	ns
Rise Time		T_r	-	23	-	
Turn-Off Delay Time		$T_{d(off)}$	-	47	-	
Fall Time		T_f	-	21	-	
Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	C_{iss}	-	705	-	pF
Output Capacitance		C_{oss}	-	85	-	
Reverse Transfer Capacitance		C_{rss}	-	20	-	
Drain-Source Body Diode	Conditions	Symbol	Min	Typ.	Max	Unit
Drain-Source Diode Forward Current	-	I_S	-	-	2	A
Diode Forward Voltage	$V_{GS}=0V, I_S=2A, T_J=25^\circ C$	V_{SD}	-	-	1.2	V

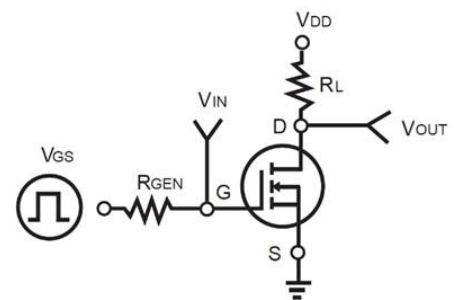
Note:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$
3. Guaranteed by design, not subject to production testing.
4. Limited only by maximum temperature allowed.
5. Pulse Width Limited by safe operating area.

Switching Time Waveform

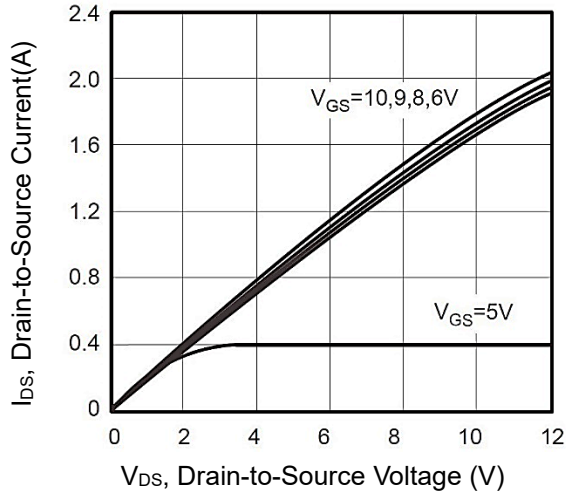


Switching Test Circuit

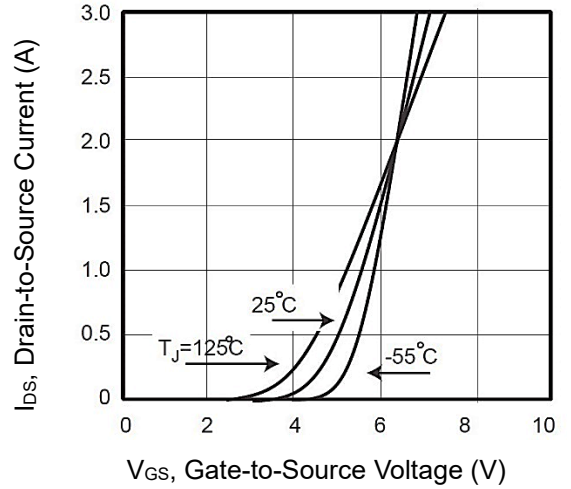


CHARACTERISTIC CURVES

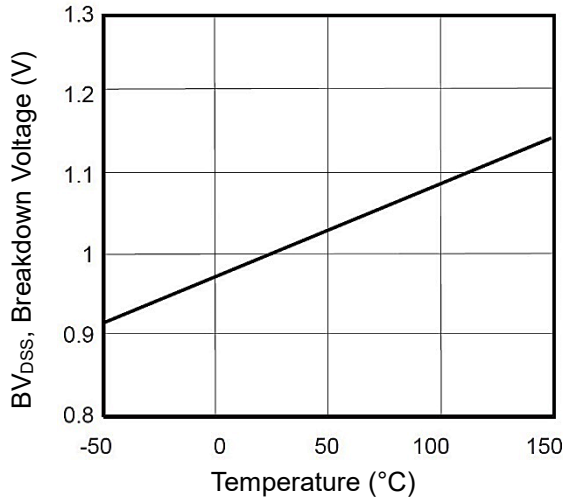
Output Characteristics



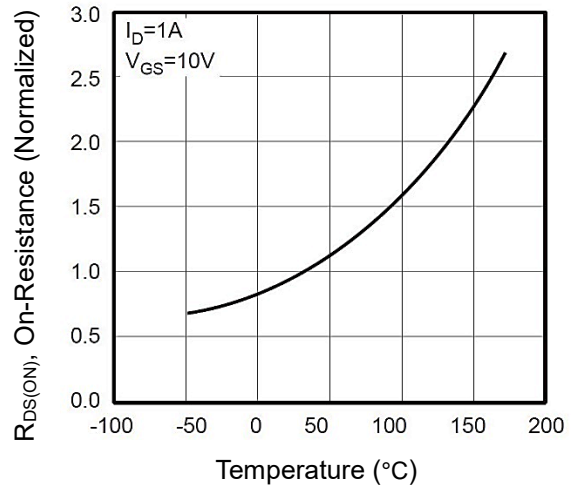
Transfer Characteristics



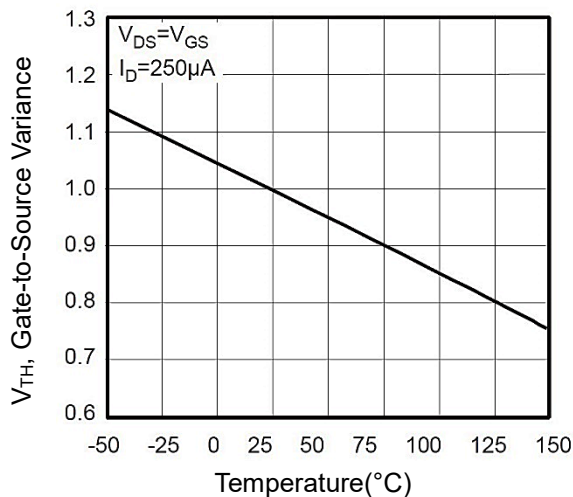
Breakdown Voltage vs. Temperature



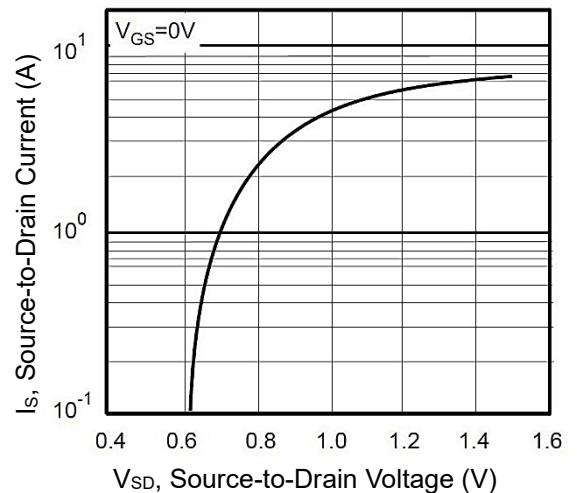
On-Resistance vs. Junction temperature



Threshold Voltage Variation with Temperature



Body Diode Characteristics



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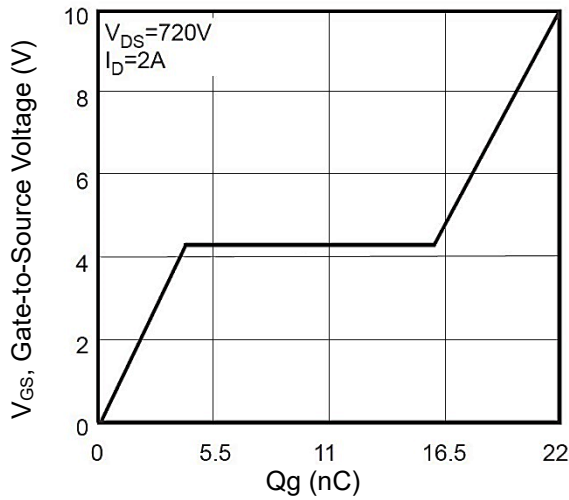
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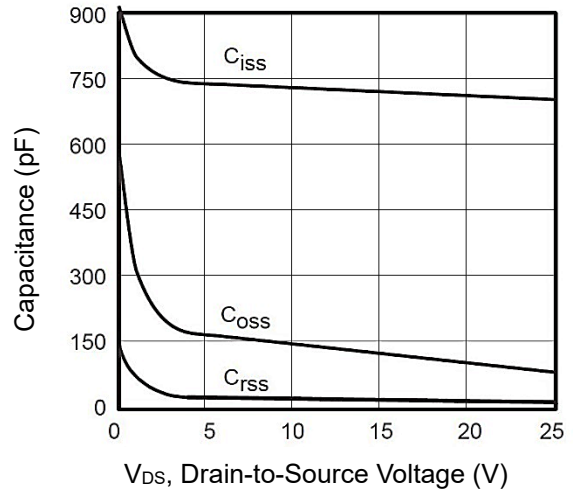
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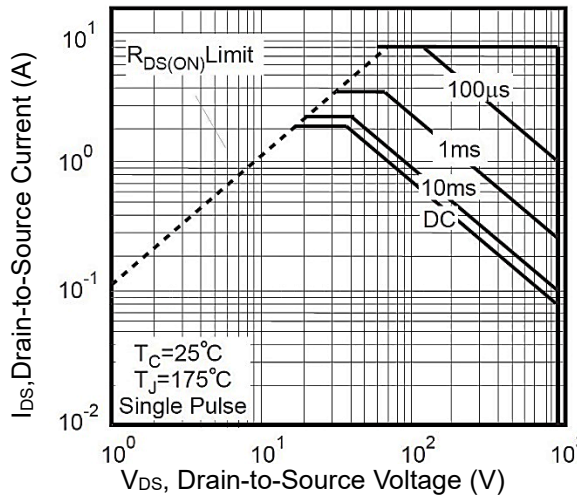
Gate-Charge Characteristics



Capacitance vs. Drain-Source Voltage



Maximum Safe Operating Area



Normalized Transient Thermal Impedance vs Pulse Width

