

# ESD Suppressor 7V Bidirectional DFN0603

ME71B15D01

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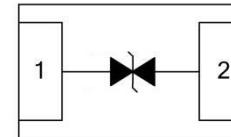
## FEATURE

- IEC 61000-4-2 ESD:  $\pm 15\text{KV}$  (Air)  $\pm 12\text{KV}$  (Contact)
- IEC 61000-4-4 EFT: 40A (5/50ns)
- IEC 61000-4-5 Lightning: 2A (8/20 $\mu\text{s}$ )
- ESD Protection for one Bidirectional Channel
- Low Capacitance and Clamping Voltage
- Solid-State Silicon-Avalanche Technology



## APPLICATION

- Computer Interfaces Protection
- Microprocessors Protection
- Serial and Parallel Ports Protection
- Control Signal Lines Protection
- Power Lines on PCB Protection



## MAXIMUM RATINGS AND CHARACTERISTICS



| Parameter  | Symbol          | Value    | Unit                 |
|--|-----------------|----------|----------------------|
| ESD Voltage per IEC61000-4-2 (Contact discharge) | $V_{ESD}$       | $\pm 12$ | KV                   |
| ESD Voltage per IEC61000-4-2 (Air discharge)     |                 | $\pm 15$ | KV                   |
| Typical Thermal Resistance                       | $R_{\theta JA}$ | 500      | $^{\circ}\text{C/W}$ |
| Operating & Storage Temperature Range            | $T_J, T_{STG}$  | -55~+150 | $^{\circ}\text{C}$   |

## ELECTRICAL CHARACTERISTICS

| Parameter                      | Condition                              | Symbol    | Min. | Typ. | Max. | Unit          |
|--------------------------------|--|-----------|------|------|------|---------------|
| Reverse Stand-Off Voltage      | --                                     | $V_{RWM}$ | --   | --   | 7    | V             |
| Reverse Breakdown Voltage      | $I_{BR}=1\text{mA}$                    | $V_{BR}$  | 8    | --   | 10   | V             |
| Reverse Leakage Current        | $V_R=7\text{V}$                        | $I_R$     | --   | --   | 1    | $\mu\text{A}$ |
| Clamping Voltage               | $I_{PP}=1\text{A}, tp=8/20\mu\text{s}$ | $V_C$     | --   | --   | 13   | V             |
|                                | $I_{PP}=2\text{A}, tp=8/20\mu\text{s}$ |           | --   | --   | 15   |               |
| Clamping Voltage TLP           | $I_{PP}=8\text{A}, tp=100\text{ns}$    | $V_C$     | --   | 16.8 | --   | V             |
|                                | $I_{PP}=16\text{A}, tp=100\text{ns}$   |           | --   | 22.7 | --   |               |
| Dynamic Resistance             | $tp=100\text{ns}$                      | $R_{DYN}$ | --   | 0.74 | --   | $\Omega$      |
| Off State Junction Capacitance | $V_{dc}=0, f=1\text{MHz}$              | $C_J$     | --   | --   | 10   | pF            |

Notes:

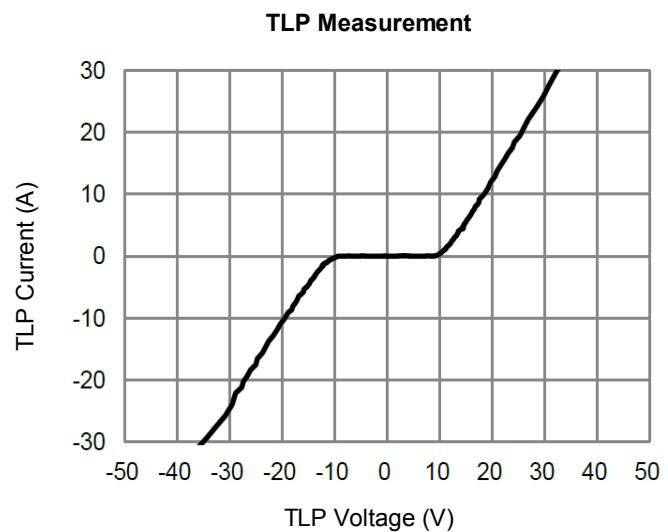
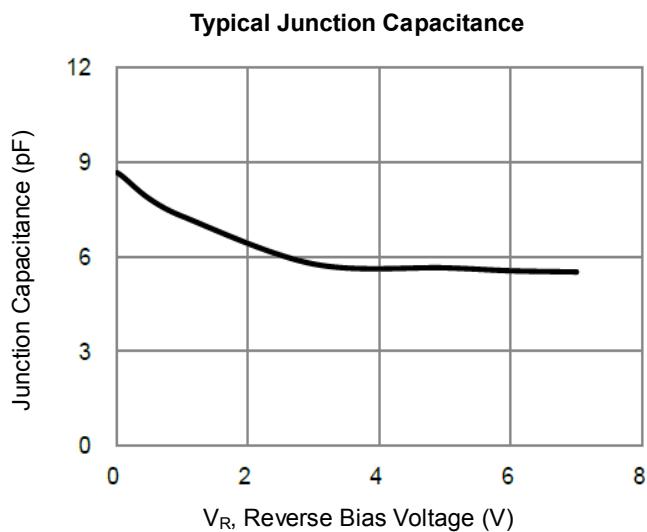
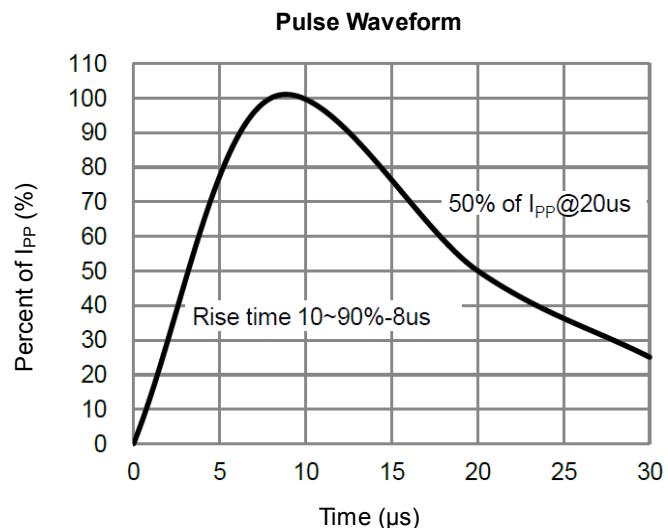
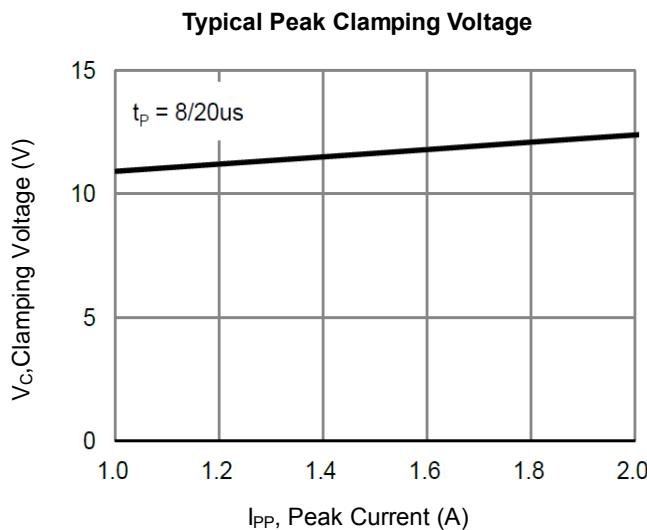
1.  $T_J=25^{\circ}\text{C}$  unless otherwise specified
2. Mounted on a FR4 PCB, Single-sided copper, mini pad
3. Transmission Line Pulse (TLP) test condition:  $Z_0=50\Omega, tp=100\text{ns}$

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## CHARACTERISTIC CURVES



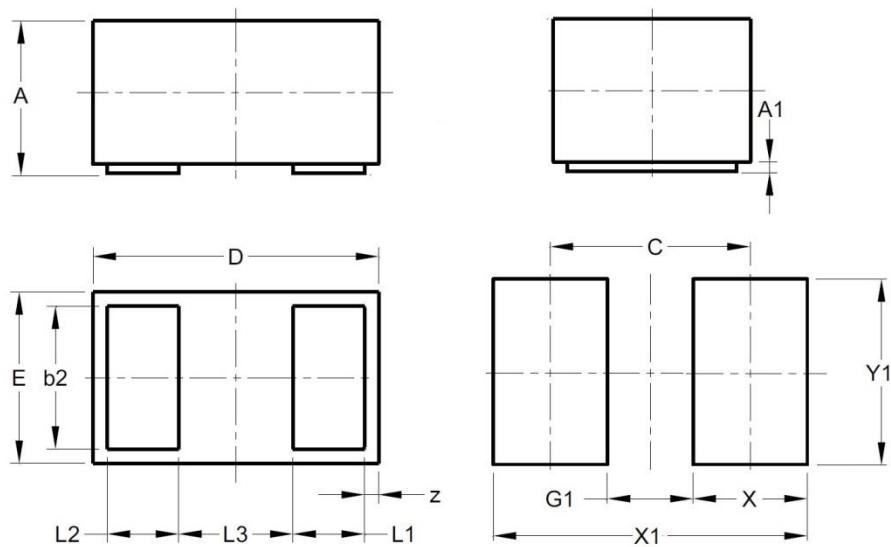
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## DIMENSIONS AND RECOMMENDED LAND PATTERN

| Item | Min (mm) | Max (mm) |
|------|----------|----------|
| A    | 0.27     | 0.37     |
| A1   | -        | -        |
| b2   | 0.20     | 0.28     |
| D    | 0.57     | 0.67     |
| E    | 0.27     | 0.37     |
| z    | 0.10     | 0.10     |
| L1   | 0.10     | 0.18     |
| L2   | 0.10     | 0.18     |
| L3   | 0.24     | 0.24     |
| C    | 0.40     | 0.40     |
| G1   | 0.16     | 0.16     |
| X    | 0.24     | 0.24     |
| X1   | 0.64     | 0.64     |
| Y1   | 0.34     | 0.34     |



\*Specifications subject to change without notice.