# **Multilayer Ceramic Capacitors**

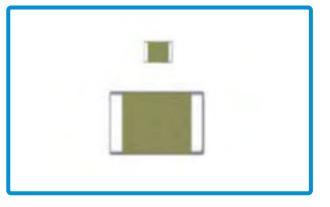
## **OPEN-MODE DESIGN**



**OP** Series

#### **FEATURES**

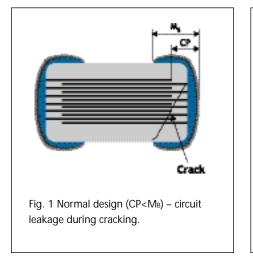
- Minimize the short curcuit probability due to flex cracks
- High current applications
- Input side filtering



### PART NUMBER SYSTEM

			OP	<u>0805</u>	XR	<u>101</u>	K	<u>500</u>
Meritek Se	eries							
Size								
Dielectric								
CODE	XR							
	X7R							
Capacitan	се							
				igits. Third digit	denotes numb	ber		
of zeros to f	ollow. Use R	for decimal poi	nt for values le	ss than 10pF.				
CODE	8R2	101						
pF	8.2	100						
Tolerance								
CODE	В	С	D	G	J	ĺ		
	±.10pF	±.25pF	±.50pF	±2%	±5%			
For values I	ess than 10 p	F use C or D						
Rated Vol	age							
	t digits + num	ber of zeros.						
CODE	<u> </u>	500						
	25V 5	50V						

#### ■ INNER CONSTRUCTION OF OPEN-MODE DESIGN



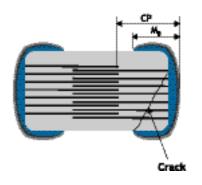


Fig. 2 Open-mode design (CP>MB) – circuit open during cracking.

